DU2810S



RF Power MOSFET Transistor 10W, 2-175MHz, 28V

M/A-COM Products Released; RoHS Compliant

Features

- N-Channel enhancement mode device
- DMOS structure
- Lower capacitances for broadband operation
- · Common source configuration
- Low noise floor

ABSOLUTE MAXIMUM RATINGS AT 25° C

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DS}	65	V
Gate-Source Voltage	V _{GS}	20	V
Drain-Source Current	I _{DS}	2.8	Α
Power Dissipation	P_D	35	W
Junction Temperature	TJ	200	°C
Storage Temperature	T _{STG}	-65 to +150	°C
Thermal Resistance	θ_{JC}	2	°C/W

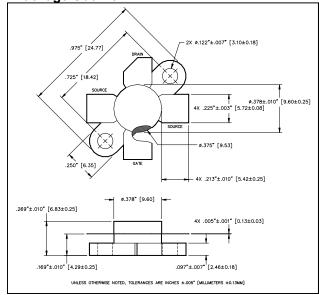
TYPICAL DEVICE IMPEDANCE

F (MHz)	Z _{IN} (Ω)	Z_{LOAD} (Ω)			
30	20 - j11.0	23.0 + j3.0			
50	24.0 - j15.0	19.0 +j5.0			
100	18.0 - j18.0	14.0 + j6.0			
200	12.0—j19.0	9.0 + j5.0			
V _{DD} = 28V, I _{DQ} = 100mA, P _{OUT} = 10.0W					

 $Z_{\mbox{\scriptsize IN}}$ is the series equivalent input impedance of the device from gate to source.

 Z_{LOAD} is the optimum series equivalent load impedance as measured from drain to ground.

Package Outline



LETTER	MILLIM	ETERS	INCHES	
DIM	MIN	MAX	MIN	MAX
Α	24.64	24.89	.970	.980
В	18.29	18.54	.720	.730
С	20.07	20.83	.790	.820
D	9.47	9.73	.373	.383
E	6.22	6.48	.245	.255
F	5.64	5.79	.222	.228
G	2.92	3.30	.115	.130
Н	2.29	2.67	.090	.105
J	4.04	4.55	.159	.179
К	6.58	7.39	.259	.291
L	.10	.15	.004	.006

ELECTRICAL CHARACTERISTICS AT 25°C

Parameter	Symbol	Min	Max	Units	Test Conditions
Drain-Source Breakdown Voltage	BV _{DSS}	65	-	V	$V_{GS} = 0.0 \text{ V}$, $I_{DS} = 4.0 \text{ mA}$
Drain-Source Leakage Current	I _{DSS}	-	2.0	mA	V _{GS} = 28.0 V , V _{GS} = 0.0 V
Gate-Source Leakage Current	I _{GSS}	-	2.0	μΑ	V _{GS} = 20.0 V , V _{DS} = 0.0 V
Gate Threshold Voltage	$V_{GS(TH)}$	2.0	6.0	V	V _{DS} = 10.0 V , I _{DS} = 20 mA
Forward Transconductance	G_{M}	160	-	S	V_{DS} = 10.0 V , I_{DS} = 200 mA , Pulsed 80-300 μs
Input Capacitance	C _{ISS}	-	14	pF	V _{DS} = 28.0 V , F = 1.0 MHz
Output Capacitance	Coss	-	10	pF	V _{DS} = 28.0 V , F = 1.0 MHz
Reverse Capacitance	C _{RSS}	-	4.6	pF	V _{DS} = 28.0 V , F = 1.0 MHz
Power Gain	G _P	13	-	dB	V _{DD} = 28.0 V, I _{DQ} = 100 mA, P _{OUT} = 10 W F =175 MHz
Drain Efficiency	ŋ₀	55	-	%	V _{DD} = 28.0 V, I _{DQ} = 100 mA, P _{OUT} = 10 W F =175 MHz
Load Mismatch Tolerance	VSWR-T	-	20:1	-	V _{DD} = 28.0 V, I _{DQ} = 100 mA, P _{OUT} = 10 W F =175 MHz

ADVANCED: Data Sheets contain information regarding a product M/A-COM Technology Solutions is considering for development. Performance is based on target specifications, simulated results, and/or prototype measurements. Commitment to develop is not guaranteed.

PRELIMINARY: Data Sheets contain information regarding a product M/A-COM Technology

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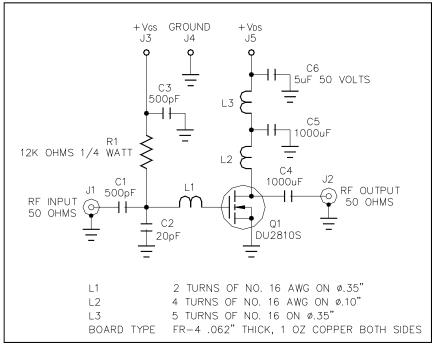
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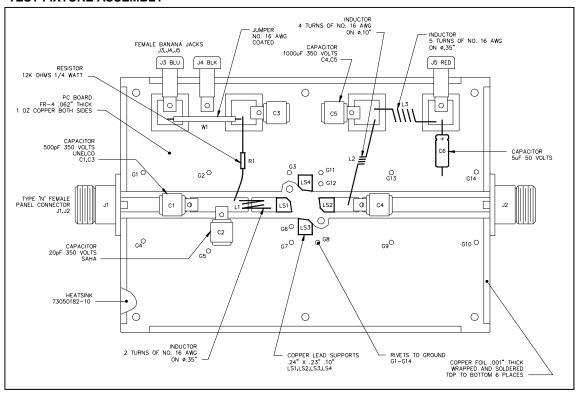
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TEST FIXTURE SCHEMATIC



TEST FIXTURE ASSEMBLY



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