

Digital Attenuator 15.0 dB, 4-Bit, TTL Driver, DC-4.0 GHz

Rev. V5

Electrical Specifications: $T_A = 25^\circ\text{C}$

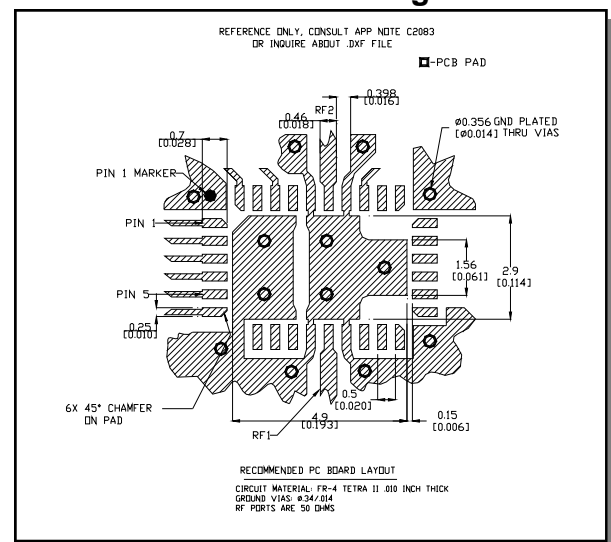
Parameter	Test Conditions	Frequency	Units	Min	Typ	Max
Insertion Loss	—	DC-2.5 GHz	dB	—	2.0	2.5
		DC-4.0 GHz	dB	—	2.5	3.0
Attenuation Accuracy	Individual Bits or Combination of Bits	DC-2.5 GHz	dB	—	—	$\pm(0.3+4\%$ of atten setting)
		DC-4.0 GHz	dB	—	—	$\pm(0.3+6\%$ of atten setting)
VSWR	Full Attenuation Range	DC-2.5 GHz	Ratio	—	1.5:1	1.8:1
		DC-4.0 GHz	Ratio	—	1.8:1	2.0:1
Switching Speed	50% Cntl to 90%/10% RF 10% to 90% or 90% to 10%	—	ns	—	25	—
		—	ns	—	4	—
1 dB Compression	—	50 MHz	dB	—	+21	—
		0.5-4.0 GHz	dB	—	+27	—
Input IP_3	Two-tone Inputs up to +5 dBm	50 MHz	dB	—	+35	—
		0.5-4.0 GHz	dB	—	+48	—
+Vcc	—	—	V	4.75	5.0	5.25
-Vee	—	—	V	-8.0	-5.0	-4.75
V_{IL} V_{IH}	LOW-level input voltage HIGH-level input voltage	—	V	0.0	—	0.8
		—	V	2.0	—	5.0
I_{in} (Input Leakage Current)	$V_{in} = V_{CC}$ or GND	—	μA	-1.0	—	1.0
I_{cc} (Quiescent Supply Current)	$V_{cntrl} = V_{CC}$ or GND	—	μA	—	250	400
ΔI_{cc} (Additional Supply Current Per TTL Input Pin)	$V_{CC} = \text{Max}$, $V_{cntrl} = V_{CC} - 2.1\text{ V}$	—	mA	—	—	1.5
IEE	V_{EE} min to max, $V_{in} = V_{IL}$ or V_{IH}	—	mA	-1.0	-0.2	—
Thermal Resistance θ_{jc}	—	—	$^\circ\text{C/W}$	—	15	—

Absolute Maximum Ratings^{3,4}

Parameter	Absolute Maximum
Max. Input Power 0.05 GHz 0.5 - 4.0 GHz	+27 dBm +34 dBm
V_{CC}	$-0.5\text{ V} \leq V_{CC} \leq +7.0\text{ V}$
V_{EE}	$-8.5\text{ V} \leq V_{EE} \leq +0.5\text{ V}$
$V_{CC} - V_{EE}$	$-0.5\text{ V} \leq V_{CC} - V_{EE} \leq 14.5\text{ V}$
V_{in}^5	$-0.5\text{ V} \leq V_{in} \leq V_{CC} + 0.5\text{ V}$
Operating Temperature	-40°C to $+85^\circ\text{C}$
Storage Temperature	-65°C to $+125^\circ\text{C}$

- Exceeding any one or combination of these limits may cause permanent damage to this device.
- M/A-COM does not recommend sustained operation near these survivability limits.
- Standard CMOS TTL interface, latch-up will occur if logic signal applied prior to power supply.

Recommended PCB Configuration⁶



6. Application Note S2083 is available on line at www.macom.com

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PRELIMINARY: Data Sheets contain information regarding a product M/A-COM Technology Solutions has under development. Performance is based on engineering tests. Specifications are typical. Mechanical outline has been fixed. Engineering samples and/or test data may be available. Commitment to produce in volume is not guaranteed.

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Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

Gallium Arsenide Integrated Circuits are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these devices.

Moisture Sensitivity

The MSL rating for this part is defined as Level 2 per IPC/JEDEC J-STD-020. Parts shall be stored and/or baked as required for MSL Level 2 parts.

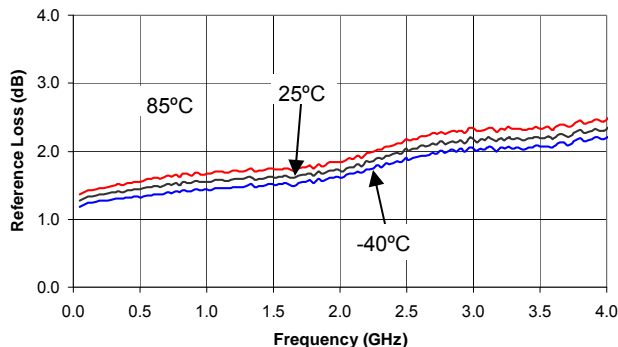
Truth Table (Digital Attenuator)

C8	C4	C2	C1	Attenuation
0	0	0	0	Loss, Reference
0	0	0	1	1.0 dB
0	0	1	0	2.0 dB
0	1	0	0	4.0 dB
1	0	0	0	8.0 dB
1	1	1	1	15.0 dB

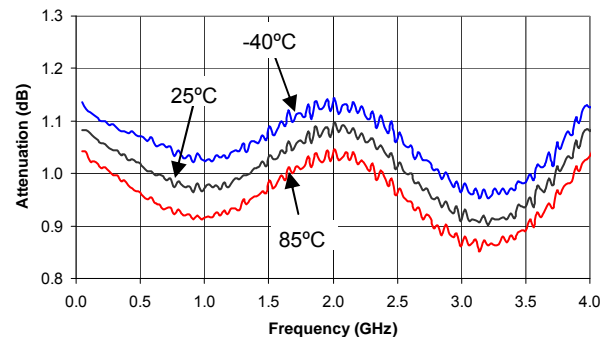
0 = TTL Low, 1 = TTL High

Typical Performance Curves

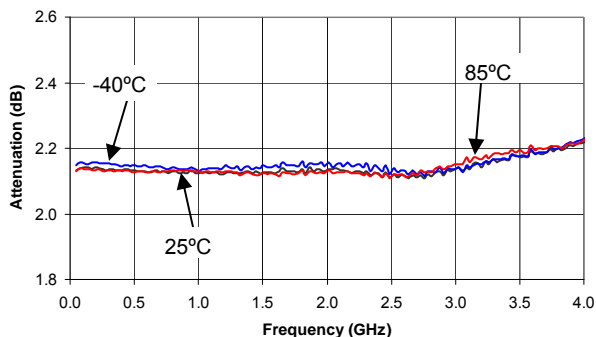
Reference Loss vs. Frequency



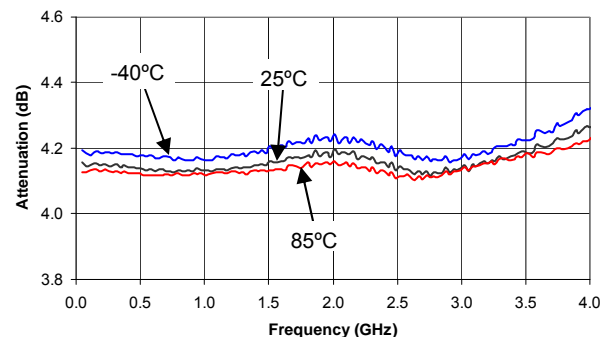
Attenuation - 1 dB Bit vs. Frequency



Attenuation - 2 dB Bit vs. Frequency

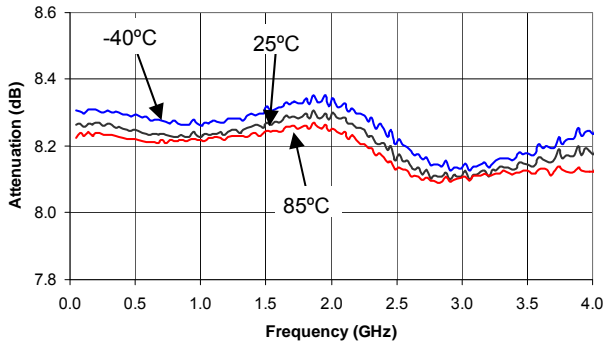


Attenuation - 4dB Bit vs. Frequency

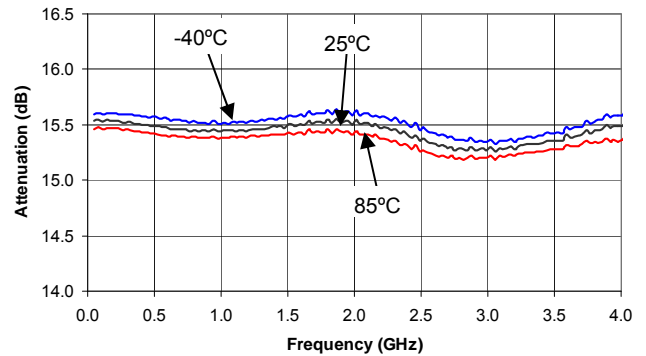


Typical Performance Curves

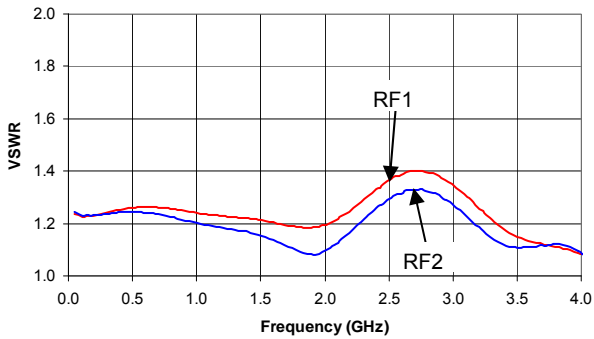
Attenuation - 8 dB Bit vs. Frequency



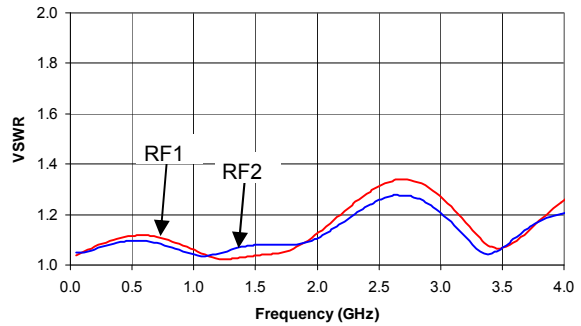
Attenuation - 15 dB Attenuation vs. Frequency



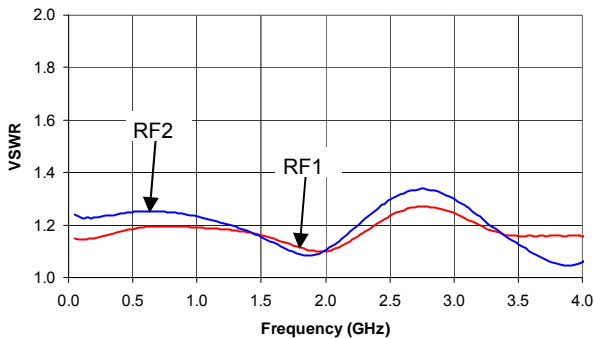
VSWR vs. Frequency
Reference Loss State



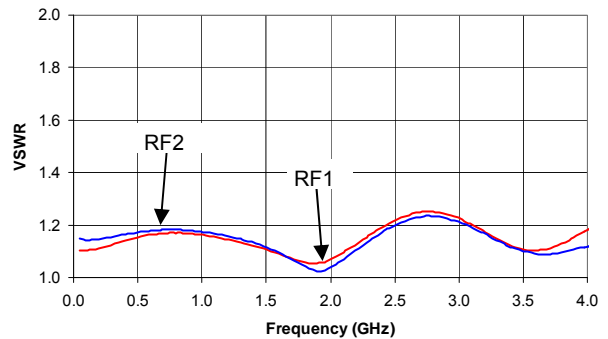
VSWR - 1 dB Bit vs. Frequency



VSWR - 2 dB Bit vs. Frequency



VSWR - 4 dB Bit vs. Frequency

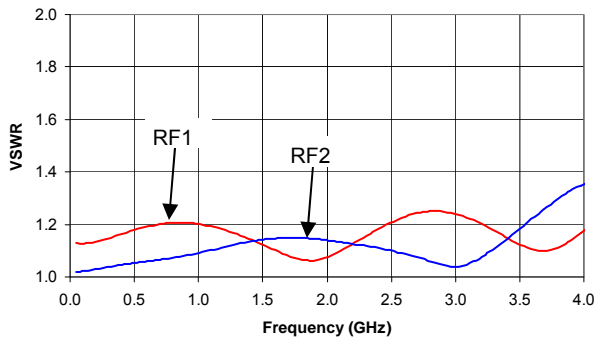


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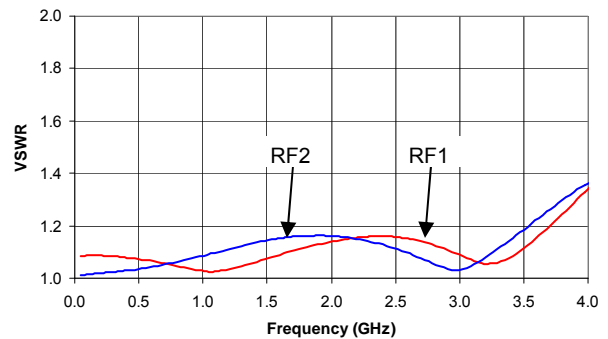
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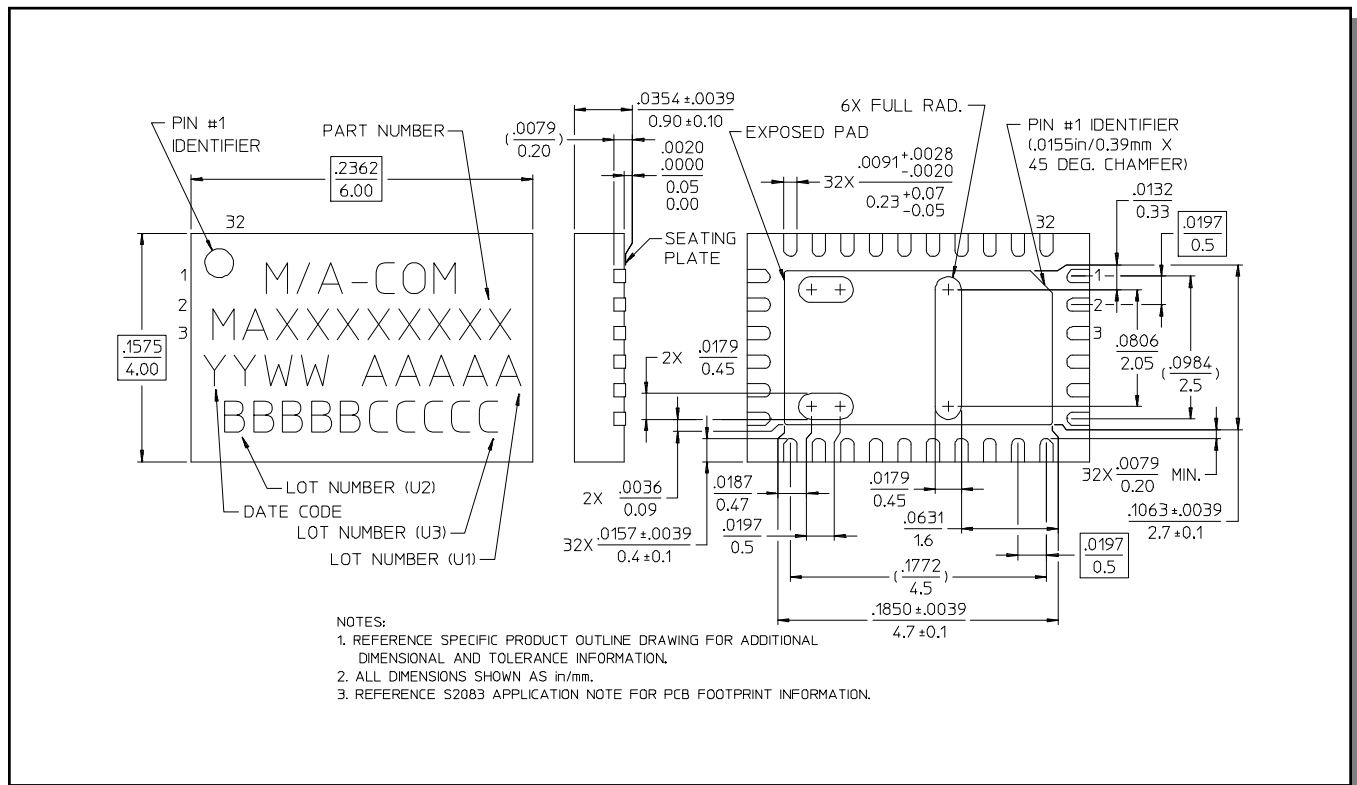
VSWR - 8 dB Bit vs. Frequency



VSWR - 15 dB Attenuation vs. Frequency



CSP-1, Lead-Free 4 x 6 mm, 32-lead PQFN†



† Reference Application Note M538 for lead-free solder reflow recommendations.