LDMOS Pulsed Power Transistor 75W, 978 MHz, 400µs Pulse, 1% Duty

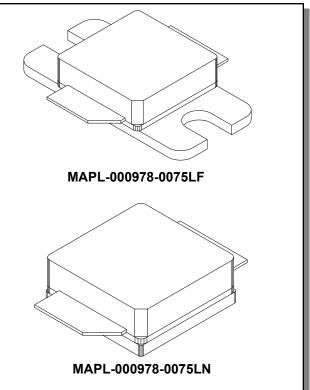


- Gold LDMOS microwave power transistor
- Common source configuration
- Broadband Class AB operation
- RoHS Compliant
- Avionics applications specifically designed for Internal input and output impedance matching.
- Integrated ESD Protection
- RoHS Compliant



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Product Image



Absolute Maximum Ratings at 25°C

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V _{DSS}	60	V
Gate-Source Voltage	V _{GS}	-0.7 to 15	V
Total Power Dissipation $(T_c = 25^{\circ}C)$	P _{TOT}	350	W
Storage Temperature	T _{STG}	-65 to +175	°C
Junction Temperature	TJ	200	°C

Thermal Characteristics

Parameter	Test Conditions	Symbol	Max	Units
Thermal Resistance, Junction to Case	V_{DD} = 28V, I_{DQ} = 250mA, Pout = 75W	R _{TH(JC)}	0.5	°C/W

Typical RF Performance

Freq.	Pin	Pout	Gain	ld-Pk	Eff	RL	VSWR-S	VSWR-T	P1dB	
(MHz)	(W)	(W)	(dB)	(A)	(%)	(dB)	(2:1)	(5:1)	Pout (W)	Gain (dB)
978	1.1	75	18.2	5.3	50	-16	S	Р	81	18.0

Typical RF performance measured in M/A-COM RF test fixture. Devices tested in common source Class-AB configuration as follows: Vdd=28V, Idq=250mA (pulsed), F=978MHz, Pulse=400us, Duty=1%.

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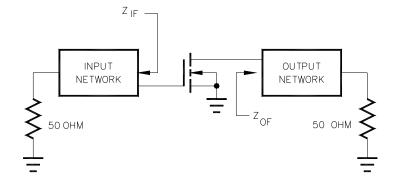
Electrical Specifications: T_c = 25 ± 5°C (Room Ambient)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Units
DC CHARACTERISTICS	•			•		
Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 30mA$	BV_{DSS}	60	-	-	V
Drain Leakage Current	V _{DS} = 28V, V _{GS} = 0V	I _{DSS}	-	-	50	μA
Gate-Source Leakage Current	V _{GS} = 10V, V _{DS} = 0V	I _{GSS}	-	-	2	μA
Gate Threshold Voltage	V _{DS} = 10V, I _D = 30mA	V _{GS (th)}	2	2.5	4	V
Forward Transconductance	V _{GS} = 10V, I _D = 1A	G _M	1.5	-	-	S
DYNAMIC CHARACTERISTIC	S					•
Input Capacitance	V_{DS} = 28V, V_{GS} = 0V, F = 1MHz (capacitance values without internal matching)	C _{iss}	-	200	-	pF
Output Capacitance	V_{DS} = 28V, V_{GS} = 0V, F = 1MHz	C _{oss}	-	83	-	pF
Reverse Transfer Capacitance	V_{DS} = 28V, V_{GS} = 0V, F = 1MHz	C _{rss}	-	3.0	_	pF
RF FUNCTIONAL TESTS						
Power Gain	$V_{DD} = 28V, I_{DQ} = 250mA, Pout = 75W$	G _P	16.5	18.2	-	dB
Drain Efficiency	$V_{DD} = 28V, I_{DQ} = 250mA, Pout = 75W$	η _D	45	50	-	%
Input Return Loss	$V_{DD} = 28V, I_{DQ} = 250mA, Pout = 75W$	RL	-	-16	-10	dB
1dB Compression Point	V _{DD} = 28V, I _{DQ} = 250mA	P1dB	-	81	-	W
Load Mismatch Stability	V_{DD} = 28V, I_{DQ} = 250mA, Pout = 75W	VSWR-S	-	-	2:1	-
Load Mismatch Tolerance	$V_{DD} = 28V, I_{DQ} = 250mA, Pout = 75W$	VSWR-T	-	-	5:1	-

Test Fixture Impedance

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F (MHz)	Z _{IF} (Ω)	Z _{OF} (Ω)
978	9.8 + j1.4	1.5 + j0.1



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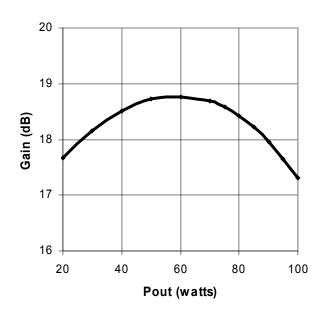
Output Power vs. Input Power



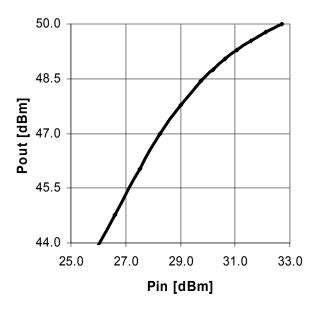
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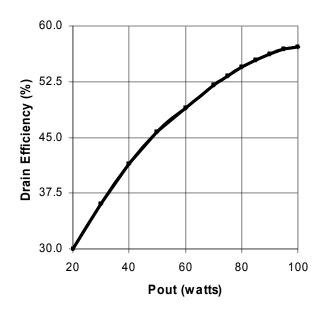
RF Power Gain vs. Output Power



Output Power vs. Input Power [dBm]



Drain Efficiency vs. Output Power



3

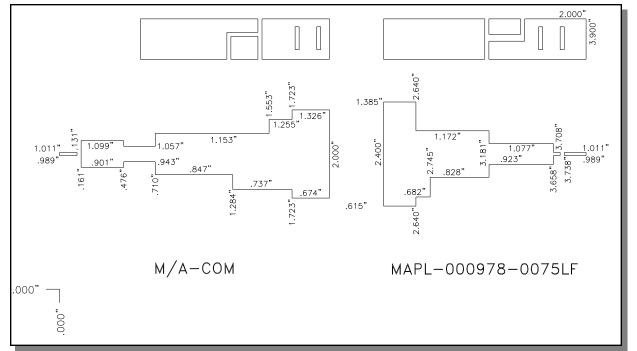
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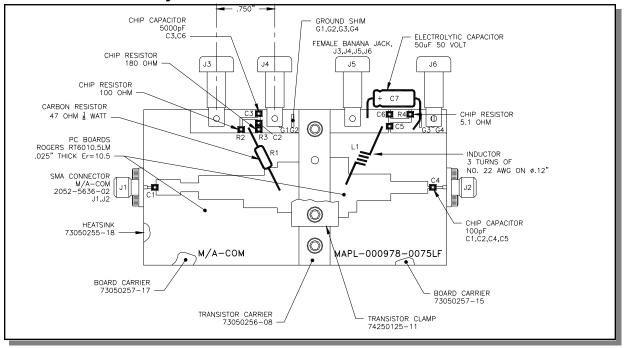
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Test Fixture Circuit Dimensions



Test Fixture Assembly

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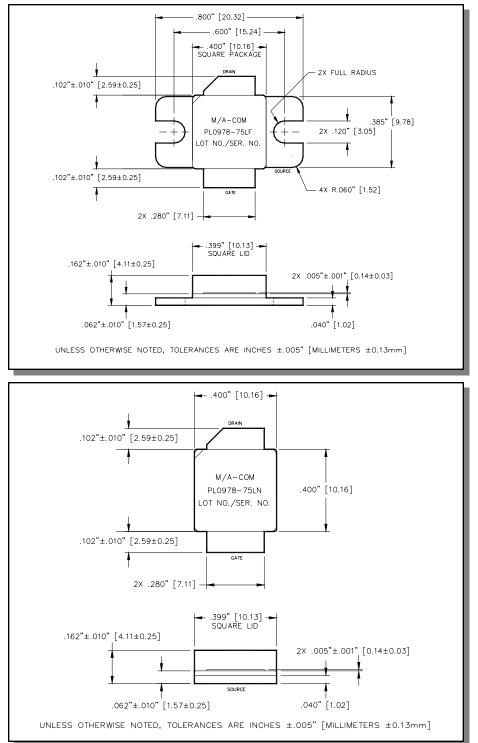
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Outline Drawings



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