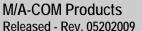


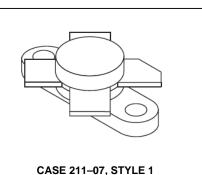
### The RF Line NPN Silicon Power Transistor 30W, 30-200MHz, 28V

Designed primarily for wideband large-signal driver and output amplifier stages in the 30-200 MHz frequency range.

- Guaranteed performance at 150 MHz, 28 Vdc Output power = 30 W Minimum gain = 10 dB
- 100% tested for load mismatch at all phase angles with 30:1 VSWR
- Gold metallization system for high reliability applications



**Product Image** 



#### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	VCEO	35	Vdc
Collector-Base Voltage	V <sub>CBO</sub>	65	Vdc
Emitter–Base Voltage	V <sub>EBO</sub>	4.0	Vdc
Collector Current — Continuous	Ι <sub>C</sub>	3.4	Adc
Total Device Dissipation @ T <sub>C</sub> = 25°C (1) Derate above 25°C	PD	82 0.47	Watts W/ºC
Storage Temperature Range	T <sub>stg</sub>	-65 to +150	°C

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case	R <sub>eJC</sub>	2.13	°C/W

#### ELECTRICAL CHARACTERISTICS (T<sub>C</sub> = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	ł			•	
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 30 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	35	—	_	Vdc
Collector–Emitter Breakdown Voltage (I <sub>C</sub> = 30 mAdc, V <sub>BE</sub> = 0)	V <sub>(BR)CES</sub>	65	_	_	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 30 mAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	65	_	_	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 3.0 mAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	4.0	_	_	Vdc
Collector Cutoff Current (V <sub>CB</sub> = 30 Vdc, I <sub>E</sub> = 0)	ICBO	_	_	3.0	mAdc

#### DC Current Gain 20 80 $h_{FE}$ (I<sub>C</sub> = 1.5 Adc, V<sub>CE</sub> = 5.0 Vdc)

v be

NOTE:

1. These devices are designed for RF operation. The total device dissipation rating applies only when the devices are operated as RF amplifiers.

- 1
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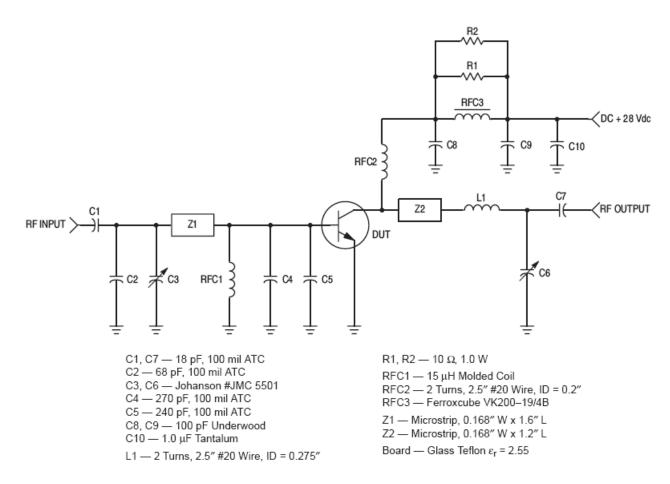


# The RF Line NPN Silicon Power Transistor 30W, 30-200MHz, 28V

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ELECTRICAL CHARACTERISTICS — continued (To = 25°C unless otherwise noted )

Characteristic	Symbol	Min	Тур	Max	Unit
DYNAMIC CHARACTERISTICS	ł	•			
Output Capacitance (V <sub>CB</sub> = 30 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>ob</sub>	_	30	40	pF
FUNCTIONAL TESTS (Figure 1)	ł	•			
Common–Emitter Amplifier Power Gain (V <sub>CC</sub> = 28 Vdc, P <sub>out</sub> = 30 W, f = 150 MHz)	G <sub>PE</sub>	10	13.5	_	dB
Collector Efficiency (V <sub>CC</sub> = 28 Vdc, P <sub>out</sub> = 30 W, f = 150 MHz)	η	50	-	—	%
Load Mismatch (V <sub>CC</sub> = 28 Vdc, P <sub>out</sub> = 30 W, f = 150 MHz, VSWR = 30:1 all phase angles)	Ψ	No Degradation in Power Output			



#### Figure 1. 150 MHz Test Circuit

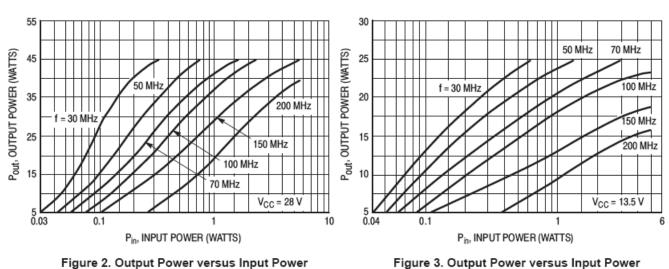
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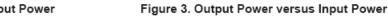


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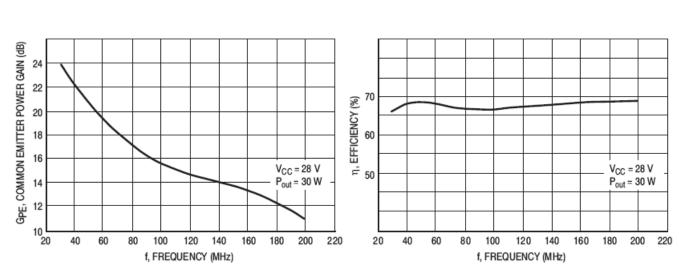


Figure 4. Power Gain versus Frequency

Figure 5. Efficiency versus Frequency

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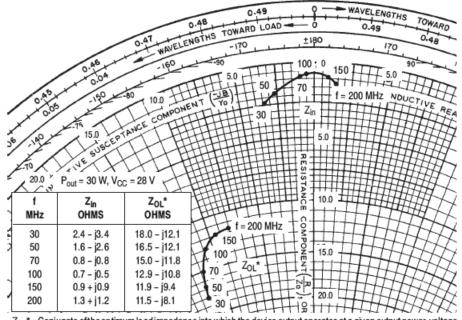
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 $Z_{OL}$ \* = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage and frequency.



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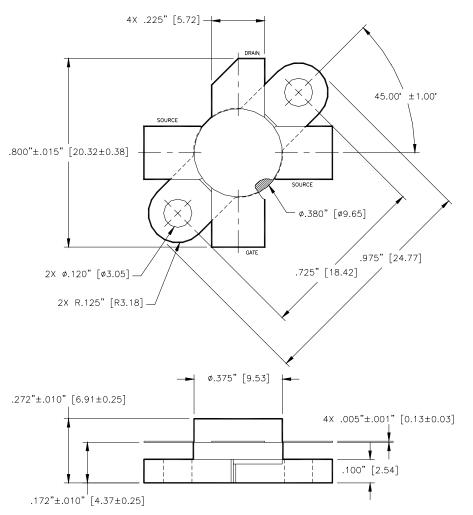
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Unless otherwise noted, tolerances are inches ±.005" [millimeters ±0.13mm]

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