

M/A-COM Products

Released - Rev. 07.07

The RF Line NPN Silicon Power Transistor 20W, 400MHz, 28V

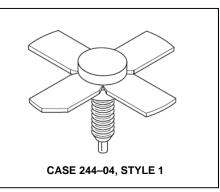
Designed primarily for wideband large–signal driver and predriver amplifier stages in the 200–500 MHz frequency range.

- Guaranteed performance at 400 MHz, 28 V Output power = 20 W Power gain = 10 dB min. Efficiency = 50% min.
- 100% tested for load mismatch at all phase angles with 30:1 VSWR
- Gold metallization system for high reliability
- Computer-controlled wirebonding gives consistent input impedance

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	V _{CEO}	33	Vdc
Collector-Base Voltage	V _{CBO}	60	Vdc
Emitter–Base Voltage	V _{EBO}	4.0	Vdc
Collector Current — Continuous — Peak	Ιc	2.2 3.0	Adc
Total Device Dissipation @ T _C = 25°C (1) Derate above 25°C	PD	55 310	Watts mW/ºC
Storage Temperature Range	T _{stg}	-65 to +150	°C

Product Image



THERMAL CHARACTERISTICS

Characteristic	Symbol	Мах	Unit
Thermal Resistance, Junction to Case	R _{θJC}	3.2	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•	•	•	ł	•
Collector–Emitter Breakdown Voltage (I _C = 20 mAdc, I _B = 0)	V _{(BR)CEO}	33	-	-	Vdc
Collector–Emitter Breakdown Voltage (I _C = 20 mAdc, V _{BE} = 0)	V _{(BR)CES}	60	-	-	Vdc
Collector–Base Breakdown Voltage (I _C = 20 mAdc, I _E = 0)	V _{(BR)CBO}	60	-	-	Vdc
Emitter–Base Breakdown Voltage (I _E = 2.0 mAdc, I _C = 0)	V _{(BR)EBO}	4.0	-	-	Vdc
Collector Cutoff Current (V _{CB} = 30 Vdc, I _E = 0)	Ісво	-	-	2.0	mAdc
ON CHARACTERISTICS	1	1	1		-

(I _C = 1.0 Adc, V _{CE} = 5.0 Vdc)		

NOTE:

Commitment to produce in volume is not gua

1. This device is designed for RF operation. The total device dissipation rating applies only when the device is operated as an RF amplifier.

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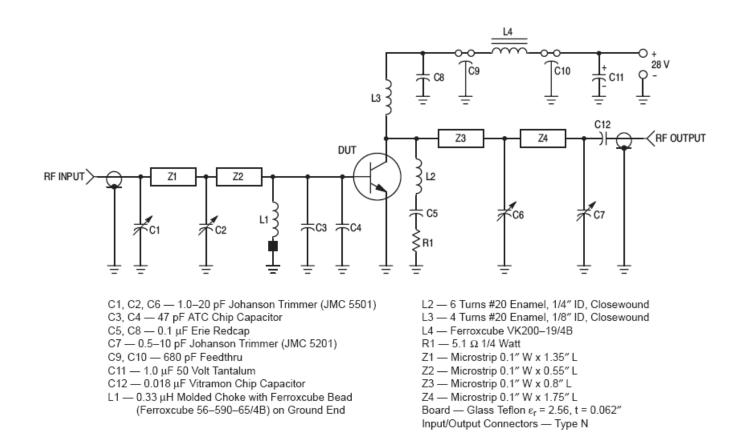
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ELECTRICAL CHARACTERISTICS — continued (T _C = 25°C unless otherwise noted.)					
Characteristic	Symbol	Min	Тур	Max	Unit
DYNAMIC CHARACTERISTICS					
Output Capacitance (V _{CB} = 28 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	-	20	24	pF
FUNCTIONAL TESTS (Figure 1)					
Common–Emitter Amplifier Power Gain (V _{CC} = 28 Vdc, P _{out} = 20 W, f = 400 MHz)	G _{PE}	10	11	-	dB
Collector Efficiency (V _{CC} = 28 Vdc, P _{out} = 20 W, f = 400 MHz)	η	50	60	-	%
Load Mismatch (V _{CC} = 28 Vdc, P _{out} = 20 W, f = 400 MHz, VSWR = 30:1 all phase angles)	Ψ	No Degradation in Output Power			





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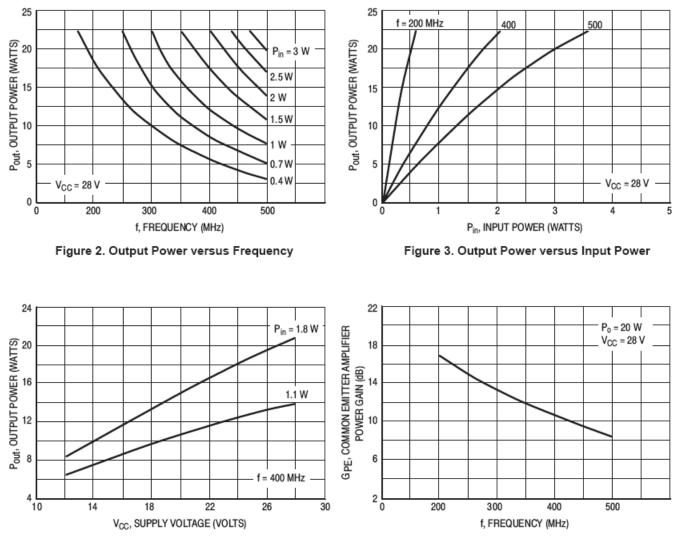


Figure 4. Output Power versus Supply Voltage

Figure 5. Power Gain versus Frequency

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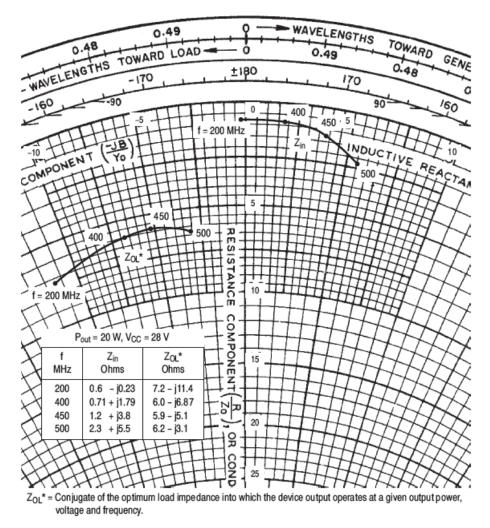


Figure 6. Series Equivalent Impedance

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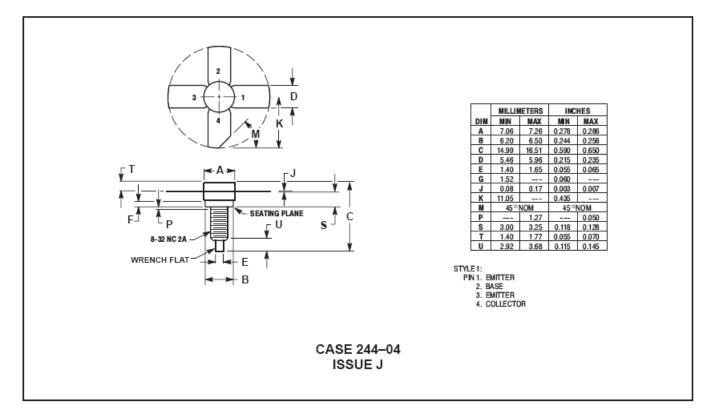
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PACKAGE DIMENSIONS



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