

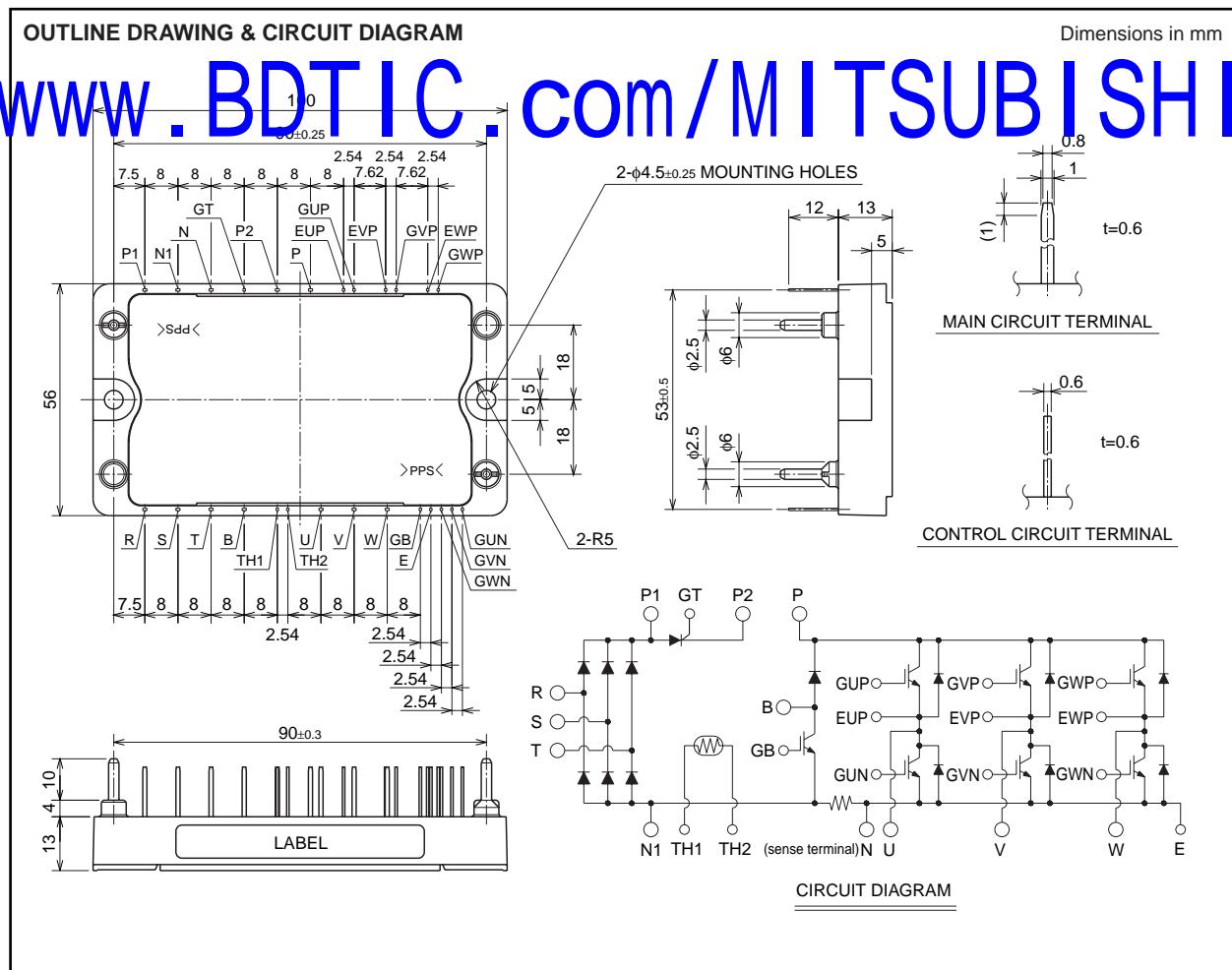
CM10AD00-12HMEDIUM POWER SWITCHING USE
FLAT BASE, INSULATED TYPE**CM10AD00-12H**

- IC 10A
- VCES 600V
- Insulated Type
- CIB Module

3φ Inverter + 3φ Converter + Brake
Thyristor + Thermistor + Current shunt
resistor

APPLICATION

AC & DC motor controls, General purpose inverters



MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$)
INVERTER PART

Symbol	Parameter	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E Short	600	V
V _{GES}	Gate-emitter voltage	C-E Short	± 20	V
I _C	Collector Current	T _C = 25°C	10	A
I _{CM}		PULSE (Note. 2)	20	A
I _E (Note.1)	Emitter Current	T _C = 25°C	10	A
I _{EM} (Note.1)		PULSE (Note. 2)	20	A
P _C (Note.3)	Maximum collector dissipation	T _C = 25°C	40	W

BRAKE PART

Symbol	Parameter	Conditions	Rating	Unit
V _{CES}	Collector-emitter voltage	G-E Short	600	V
V _{GES}	Gate-emitter voltage	C-E Short	± 20	V
I _C	Collector Current	T _C = 25°C	10	A
I _{CM}		PULSE (Note. 2)	20	A
P _C (Note.3)	Maximum collector dissipation	T _C = 25°C	39	W
V _{RMM}	Repetitive peak reverse voltage	Clamp diode part	600	V
I _{FM} (Note.3)	Forward current	Clamp diode part	10	A

CONVERTER PART

Symbol	Parameter	Conditions	Rating	Unit
V _{RMM}	Repetitive peak reverse voltage		800	V
V _{IA}	Recommended AC input voltage		220	V
I _O	DC output current	3φ rectifying circuit	10	A
I _{FSM}	Surge (non-repetitive) forward current	1/2 cycle at 60Hz, peak value, Non-repetitive	200	A
I ² t	I ² t for fusing	Value for one cycle of surge current	165	A ² s

THYRISTOR PART

Symbol	Parameter	Conditions	Rating	Unit
V _{DRM}	Repetitive peak off-state voltage		800	V
V _{RMM}	Repetitive peak reverse voltage		800	V
I _{T(AV)}	Average on-state current	Single-phase, half-wave 180° conduction	10	A
I _{TSM}	Surge (non-repetitive) on-state current	1/2 cycle at 60Hz, peak value Non-repetitive	200	A
P _{GM}	Peak gate power dissipation		10	W
P _{G(AV)}	Average gate power dissipation		1	W
I _{FGM}	Peak gate forward current		3	A
V _{FGM}	Peak gate forward voltage		10	V
V _{RGM}	Peak gate reverse voltage		5	V
di/dt	Critical rate of rise of on-state Current	I _G =100mA, V _D =400V, dI _G /dt=1A/μs	100	A/μs

COMMON RATING

Symbol	Parameter	Conditions	Rating	Unit
T _j	Junction temperature	Inverter, brake, converter part	-40 ~ +150	°C
T _j	Junction temperature	Thyristor part	-40 ~ +125	°C
T _{stg}	Storage temperature		-40 ~ +125	°C
V _{iso}	Isolation voltage	AC 1 min.	2500	V
—	Mounting torque	Mounting M4 screw	1.47 ~ 1.96	N·m
—	Weight	Typical value	120	g

ELECTRICAL CHARACTERISTICS ($T_j = 25^\circ\text{C}$)
INVERTER PART

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
ICES	Collector cutoff current	VCE = VCES, VGE = 0V	—	—	1	mA
VGE(th)	Gate-emitter threshold voltage	IC = 1.0mA, VCE = 10V	4.5	6	7.5	V
IGES	Gate-emitter cutoff current	VGE = VGES, VCE = 0V	—	—	0.5	μA
VCE(sat)	Collector-emitter saturation voltage	T _j = 25°C	IC = 10A, VGE = 15V	(Note.4)	2.1	2.8
		T _j = 150°C			2.15	—
Cies	Input capacitance	VCE = 10V VGE = 0V	—	—	1.0	nF
Coes	Output capacitance		—	—	0.9	nF
Cres	Reverse transfer capacitance	VGE = 0V	—	—	0.2	nF
QG	Total gate charge	VCC = 300V, IC = 10A, VGE = 15V	—	30	—	nC
td(on)	Turn-on delay time	VCC = 300V, IC = 10A	—	—	120	ns
tr	Turn-on rise time	VGE1 = VGE2 = 15V	—	—	300	ns
td(off)	Turn-off delay time	RG = 63Ω	—	—	200	ns
tf	Turn-off fall time	Resistive load	—	—	300	ns
V _{EC} (Note.1)	Emitter-collector voltage	I _E = 10A, VGE = 0V	—	—	2.8	V
trr (Note.1)	Reverse recovery time	I _E = 10A, VGE = 0V	—	—	110	ns
Qrr (Note.1)	Reverse recovery charge	dI _E / dt = - 20A / μs	—	0.03	—	μC
R _{th(j-c)Q}	Thermal resistance	IGBT part, Per 1/6 module	—	—	3.1	°C/W
R _{th(j-c)R}		FWDi part, Per 1/6 module	—	—	4.9	°C/W

BRAKE PART

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
ICES	Collector cutoff current	VCE = VCES, VGE = 0V	—	—	1	mA
VGE(th)	Gate-emitter threshold voltage	IC = 1.0mA, VCE = 10V	4.5	6	7.5	V
IGES	Gate-emitter cutoff current	VGE = VGES, VCE = 0V	—	—	0.5	μA
VCE(sat)	Collector-emitter saturation voltage	T _j = 25°C	IC = 10A, VGE = 15V	(Note.4)	2.1	2.8
		T _j = 150°C			2.15	—
Cies	Input capacitance	VCE = 10V VGE = 0V	—	—	1.0	nF
Coes	Output capacitance		—	—	0.9	nF
Cres	Reverse transfer capacitance	VGE = 0V	—	—	0.2	nF
QG	Total gate charge	VCC = 300V, IC = 10A, VGE = 15V	—	30	—	nC
VFM	Forward voltage drop	I _F = 10A, Clamp diode part	—	—	2.8	V
R _{th(j-c)Q}	Thermal resistance	IGBT part	—	—	3.2	°C/W
R _{th(j-c)R}		Clamp diode part	—	—	5.0	°C/W

CONVERTER PART

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
IRRM	Repetitive reverse current	VR = VRRM, T _j = 150°C	—	—	8	mA
VFM	Forward voltage drop	I _F = 10A	—	—	1.5	V
R _{th(j-c)}	Thermal resistance	Per 1/6 module	—	—	3.1	°C/W

CM10AD00-12H

**MEDIUM POWER SWITCHING USE
FLAT BASE, INSULATED TYPE**

THYRISTOR PART

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
IDRM	Repetitive peak off-state current	VD=800V	—	—	1	mA
IRRM	Repetitive peak reverse current	VR=800V	—	—	1	mA
ITM	On-state voltage	IT=10A, instantaneous means	—	—	1.45	V
IGT	Gate trigger current	VD=6V, IT=1A	—	—	50	mA
VGT	Gate trigger voltage	VD=6V, IT=1A	—	—	3	V
dv/dt	Critical rate of rise of off-state Voltage	T _j =125°C, VD=540V, exp. waveform	500	—	—	V/μs
IH	Holding current		—	50	—	mA
R _{th(j-c)}	Thermal resistance		—	—	1.75	°C/W

THERMISTOR PART

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
R _{TH}	Resistance	T _c = 25°C	—	100	—	kΩ
B	B Constant	Resistance at 25°C, 50°C (Note.5)	—	4000	—	K

RESISTOR PART

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
R	Resistance	Measured between N-N1	—	5.9	—	nΩ
α	Temperature coefficient		—	0.048	—	%/°C

COMMON RATING

Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
R _{th(c-f)}	Contact thermal resistance	Case to fin, Thermal compound applied*1 (1 module)	—	0.05	—	°C/W

Note. 1 IE, VEC, trr, Qrr, diE/dt represent characteristics of the anti-parallel, emitter to collector free-wheel diode.

2 Pulse width and repetition rate should be such that the device junction temp. (T_j) does not exceed T_{jmax} rating.

3 Junction temperature (T_j) should not increase beyond 150°C.

4 Pulse width and repetition rate should be such as to cause negligible temperature rise.

5 B = (lnR₁-lnR₂)/(1/T₁-1/T₂) R₁ : Resistance at T₁(K)

 R₂ : Resistance at T₂(K)

*1 : Typical value is measured by using Shin-etsu Silicone "G-746".