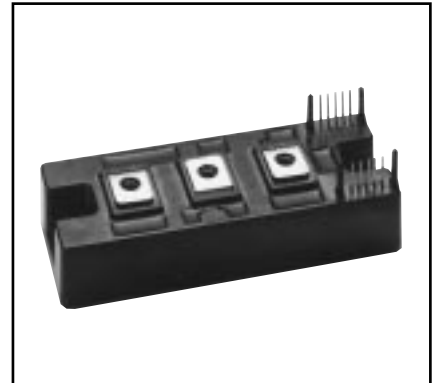
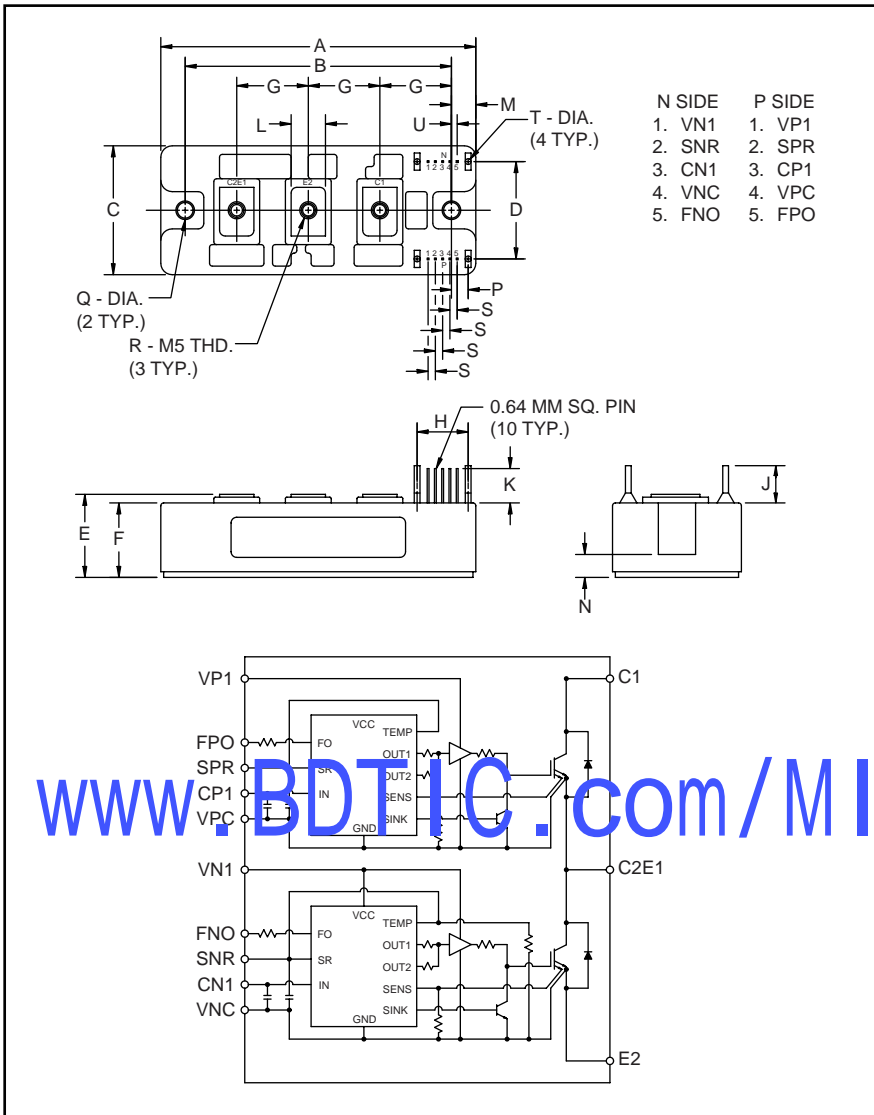


# PM100DSA120

FLAT-BASE TYPE  
INSULATED PACKAGE



### Description:

Mitsubishi Intelligent Power Modules are isolated base modules designed for power switching applications operating at frequencies to 20kHz. Built-in control circuits provide optimum gate drive and protection for the IGBT and free-wheel diode power devices.

### Features:

- Complete Output Power Circuit
- Gate Drive Circuit
- Protection Logic
  - Short Circuit
  - Over Current
  - Over Temperature
  - Under Voltage

### Applications:

- Inverters
- UPS
- Motion/Servo Control
- Power Supplies

### Ordering Information:

Example: Select the complete part number from the table below -i.e. PM100DSA120 is a 1200V, 100 Ampere Intelligent Power Module.

Type	Current Rating Amperes	V <sub>CES</sub> Volts (x 10)
PM	100	120

### Outline Drawing and Circuit Diagram

Dimensions	Inches	Millimeters
A	4.33	110.0
B	3.66±0.010	93.0±0.25
C	1.77	45.0
D	1.34	34.0
E	1.14 +0.04/-0.02	29.0 +1/-0.5
F	1.02	26.0
G	0.98	25.0
H	0.702	17.84
J	0.55	14.0
K	0.51	13.0

Dimensions	Inches	Millimeters
L	0.47	12.0
M	0.33	8.5
N	0.28	7.0
P	0.230	5.84
Q	0.22 Dia.	Dia. 5.5
R	M5 Metric	M5
S	0.100	2.54
T	0.08 Dia.	Dia. 2.0
U	0.08	2.0

**PM100DSA120**

**FLAT-BASE TYPE  
INSULATED PACKAGE**

**Absolute Maximum Ratings,  $T_j = 25^\circ\text{C}$  unless otherwise specified**

	Symbol	Ratings	Units
Power Device Junction Temperature	$T_j$	-20 to 150	$^\circ\text{C}$
Storage Temperature	$T_{\text{stg}}$	-40 to 125	$^\circ\text{C}$
Case Operating Temperature	$T_c$	-20 to 100	$^\circ\text{C}$
Mounting Torque, M5 Mounting Screws	—	2.5~3.5	N · m
Mounting Torque, M5 Main Terminal Screws	—	2.5~3.5	N · m
Module Weight (Typical)	—	340	Grams
Supply Voltage Protected by OC and SC ( $V_D = 13.5 - 16.5\text{V}$ , Inverter Part)	$V_{\text{CC(prot.)}}$	800	Volts
Isolation Voltage (Main Terminal to Baseplate, AC 1 min.)	$V_{\text{iso}}$	2500	Vrms

**Control Sector**

Supply Voltage (Applied between $V_{P1}-V_{PC}$ , $V_{N1}-V_{NC}$ )	$V_D$	20	Volts
Input Voltage (Applied between $C_{P1}-V_{PC}$ , $C_{N1}-V_{NC}$ )	$V_{\text{CIN}}$	10	Volts
Fault Output Supply Voltage (Applied between $F_{po}-V_{pc}$ and $F_{no}-V_{nc}$ )	$V_{\text{FO}}$	20	Volts
Fault Output Current (Sink Current at $F_{PO}$ , $F_{NO}$ Terminal)	$I_{\text{FO}}$	20	mA

**IGBT Inverter Sector**

Collector-Emitter Voltage ( $V_D = 15\text{V}$ , $V_{\text{CIN}} = 5\text{V}$ )	$V_{\text{CES}}$	1200	Volts
Collector Current, ( $T_c = 25^\circ\text{C}$ )	$I_c$	100	Amperes
Peak Collector Current, ( $T_c = 25^\circ\text{C}$ )	$I_{\text{CP}}$	200	Amperes
Supply Voltage (Applied between C1 - E2)	$V_{\text{CC}}$	900	Volts
Supply Voltage, Surge (Applied between C1 - E2)	$V_{\text{CC(surge)}}$	1000	Volts
Collector Dissipation	$P_c$	595	Watts

# PM100DSA120

FLAT-BASE TYPE  
INSULATED PACKAGE

## Electrical and Mechanical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
<b>Control Sector</b>						
Over Current Trip Level Inverter Part	OC	$-20^\circ\text{C} \leq T \leq 125^\circ\text{C}$ , $V_D = 15\text{V}$	145	230	—	Amperes
Short Circuit Trip Level Inverter Part	SC	$-20^\circ\text{C} \leq T \leq 125^\circ\text{C}$ , $V_D = 15\text{V}$	200	340	—	Amperes
Over Current Delay Time	$t_{\text{off}}(\text{OC})$	$V_D = 15\text{V}$	—	5	—	$\mu\text{s}$
Over Temperature Protection	OT	Trip Level	100	110	120	$^\circ\text{C}$
	$\text{OT}_r$	Reset Level	85	95	105	$^\circ\text{C}$
Supply Circuit Under Voltage Protection	UV	Trip Level	11.5	12.0	12.5	Volts
	$\text{UV}_r$	Reset Level	—	12.5	—	Volts
Supply Voltage	$V_D$	Applied between $V_{P1}$ - $V_{PC}$ , $V_{N1}$ - $V_{NC}$	13.5	15	16.5	Volts
Circuit Current	$I_D$	$V_D = 15\text{V}$ , $V_{\text{CIN}} = 5\text{V}$ , $V_{N1}$ - $V_{NC}$	—	19	26	mA
		$V_D = 15\text{V}$ , $V_{\text{CIN}} = 5\text{V}$ , $V_{\text{XP1}}$ - $V_{\text{XPC}}$	—	19	26	mA
Input ON Threshold Voltage	$V_{\text{th(on)}}$	Applied between	1.2	1.5	1.8	Volts
Input OFF Threshold Voltage	$V_{\text{th(off)}}$	$C_{P1}$ - $V_{PC}$ , $C_{N1}$ - $V_{NC}$	1.7	2.0	2.3	Volts
PWM Input Frequency	$f_{\text{PWM}}$	3- $\phi$ Sinusoidal	—	15	20	kHz
Fault Output Current	$I_{\text{FO(H)}}$	$V_D = 15\text{V}$ , $V_{\text{FO}} = 15\text{V}$	—	—	0.01	mA
	$I_{\text{FO(L)}}$	$V_D = 15\text{V}$ , $V_{\text{FO}} = 15\text{V}$	—	10	15	mA
Minimum Fault Output Pulse Width	$t_{\text{FO}}$	$V_D = 15\text{V}$	1.0	1.8	—	ms
SXR Terminal Output Voltage	$V_{\text{SXR}}$	$-20^\circ\text{C} \leq T_j \leq 125^\circ\text{C}$ , $F_{\text{in}} = 6.3\text{kHz}$ (Sp3, Sp4, Sp5, Sp6, Sp7, Sp8, Sp9, Sp10, Sp11, Sp12, Sp13, Sp14, Sp15, Sp16, Sp17, Sp18, Sp19, Sp20, Sp21, Sp22, Sp23, Sp24, Sp25, Sp26, Sp27, Sp28, Sp29, Sp30, Sp31, Sp32, Sp33, Sp34, Sp35, Sp36, Sp37, Sp38, Sp39, Sp40, Sp41, Sp42, Sp43, Sp44, Sp45, Sp46, Sp47, Sp48, Sp49, Sp50, Sp51, Sp52, Sp53, Sp54, Sp55, Sp56, Sp57, Sp58, Sp59, Sp60, Sp61, Sp62, Sp63, Sp64, Sp65, Sp66, Sp67, Sp68, Sp69, Sp70, Sp71, Sp72, Sp73, Sp74, Sp75, Sp76, Sp77, Sp78, Sp79, Sp80, Sp81, Sp82, Sp83, Sp84, Sp85, Sp86, Sp87, Sp88, Sp89, Sp90, Sp91, Sp92, Sp93, Sp94, Sp95, Sp96, Sp97, Sp98, Sp99, Sp100)	5.1	5.1	5.6	Volts

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## PM100DSA120

FLAT-BASE TYPE  
INSULATED PACKAGEElectrical and Mechanical Characteristics,  $T_j = 25^\circ\text{C}$  unless otherwise specified

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
<b>IGBT Inverter Sector</b>						
Collector Cutoff Current	$I_{CES}$	$V_{CE} = V_{CEX}, T_j = 25^\circ\text{C}$	—	—	1	mA
		$V_{CE} = V_{CEX}, T_j = 125^\circ\text{C}$	—	—	10	mA
Emitter-Collector Voltage	$V_{EC}$	$-I_C = 100\text{A}, V_D = 15\text{V}, V_{CIN} = 5\text{V}$	—	2.5	3.5	Volts
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$V_D = 15\text{V}, V_{CIN} = 0\text{V}, I_C = 100\text{A}$	—	2.3	3.2	Volts
		$V_D = 15\text{V}, V_{CIN} = 0\text{V}, I_C = 100\text{A}, T_j = 125^\circ\text{C}$	—	2.1	2.9	Volts
Inductive Load Switching Times	$t_{on}$		0.5	1.4	2.5	$\mu\text{s}$
	$t_{rr}$	$V_D = 15\text{V}, V_{CIN} = 0 \leftrightarrow 5\text{V}$	—	0.2	0.4	$\mu\text{s}$
	$t_{C(on)}$	$V_{CC} = 600\text{V}, I_C = 100\text{A}$	—	0.4	1.0	$\mu\text{s}$
	$t_{off}$	$T_j = 125^\circ\text{C}$	—	2.5	3.5	$\mu\text{s}$
	$t_{C(off)}$		—	0.6	1.1	$\mu\text{s}$

## Thermal Characteristics

Characteristic	Symbol	Condition	Min.	Typ.	Max.	Units
Junction to Case Thermal Resistance	$R_{th(j-c)Q}$	Each IGBT	—	—	0.21	$^\circ\text{C}/\text{Watt}$
	$R_{th(j-c)F}$	Each FWDi	—	—	0.35	$^\circ\text{C}/\text{Watt}$
Contact Thermal Resistance	$R_{th(c-f)}$	Case to Fin Per Module, Thermal Grease Applied	—	—	0.060	$^\circ\text{C}/\text{Watt}$

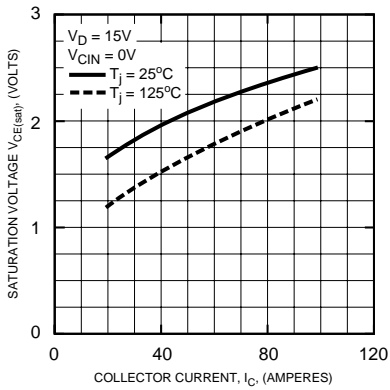
## Recommended Conditions for Use

Characteristic	Symbol	Condition	Value	Units
Supply Voltage	$V_{CC}$	Applied across C1-E2 Terminals	0 ~ 800	Volts
	$V_D$	Applied between $V_{P1}-V_{PC}, V_{N1}-V_{NC}$	$15 \pm 1.5$	Volts
Input ON Voltage	$V_{CIN(on)}$	Applied between	0 ~ 0.8	Volts
Input OFF Voltage	$V_{CIN(off)}$	$C_{P1}-V_{PC}, C_{N1}-V_{NC}$	$4.0 \sim V_{SXR}$	Volts
PWM Input Frequency	$f_{PWM}$	Using Application Circuit	5 ~ 20	kHz
Minimum Dead Time	$t_{dead}$	Input Signal	$\geq 3.5$	$\mu\text{s}$

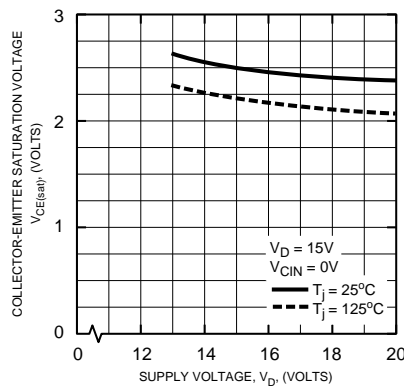
# PM100DSA120

FLAT-BASE TYPE  
INSULATED PACKAGE

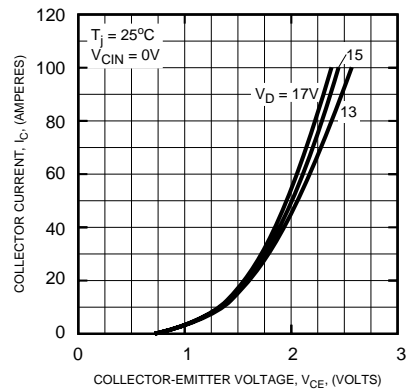
**SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)**



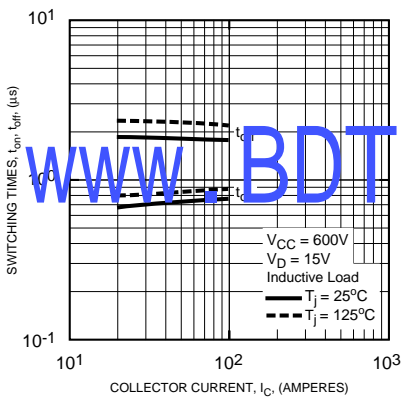
**COLLECTOR-EMITTER SATURATION VOLTAGE CHARACTERISTICS (TYPICAL)**



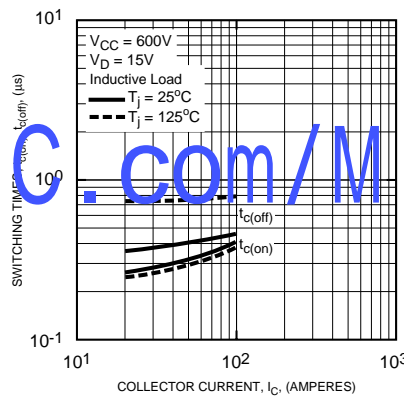
**OUTPUT CHARACTERISTICS (TYPICAL)**



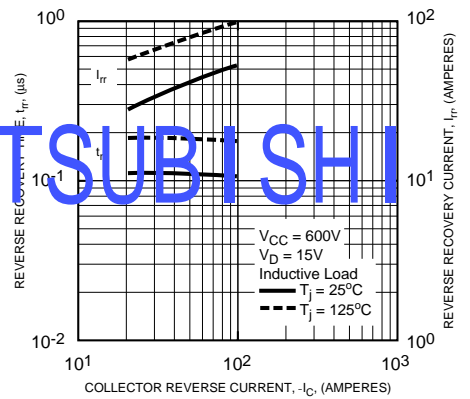
**SWITCHING TIME VS. COLLECTOR CURRENT (TYPICAL)**



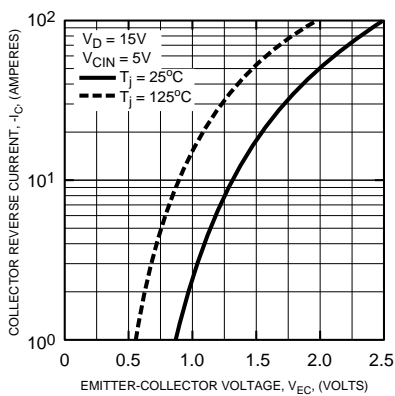
**SWITCHING TIME VS. COLLECTOR CURRENT (TYPICAL)**



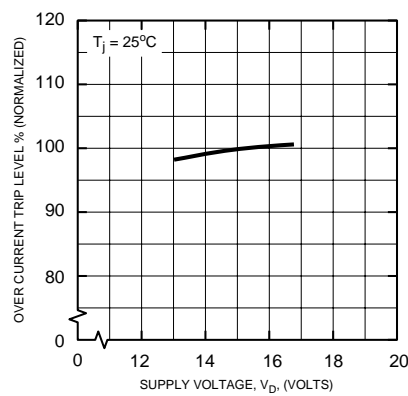
**REVERSE RECOVERY CURRENT VS. COLLECTOR CURRENT (TYPICAL)**



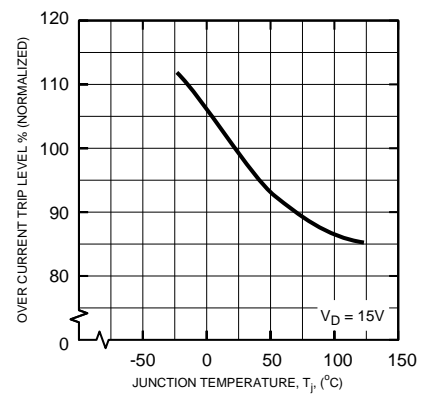
**DIODE FORWARD CHARACTERISTICS**



**OVER CURRENT TRIP LEVEL VS. SUPPLY VOLTAGE (TYPICAL)**



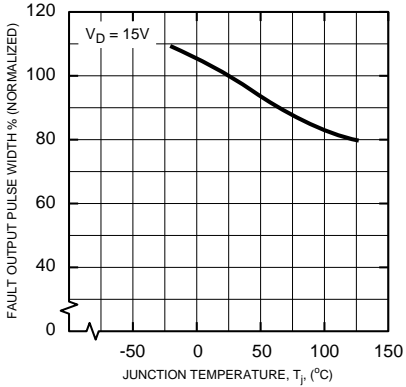
**OVER CURRENT TRIP LEVEL TEMPERATURE DEPENDENCY (TYPICAL)**



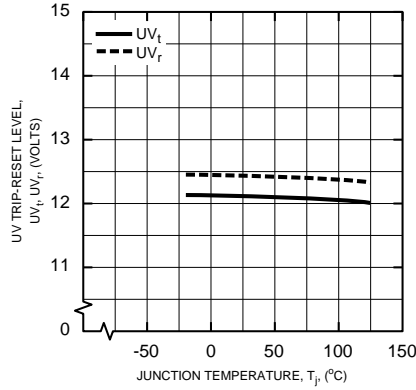
# PM100DSA120

FLAT-BASE TYPE  
INSULATED PACKAGE

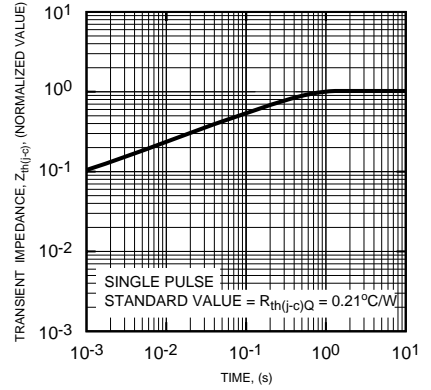
**FAULT OUTPUT PULSE WIDTH VS. TEMPERATURE (TYPICAL)**



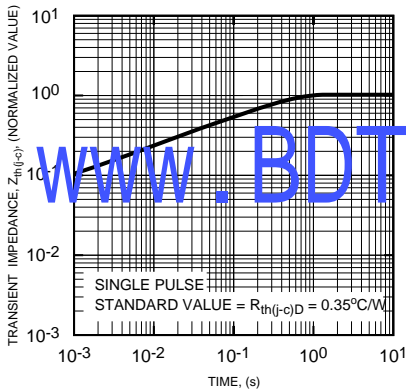
**CONTROL SUPPLY VOLTAGE TRIP-RESET LEVEL TEMPERATURE DEPENDENCY (TYPICAL)**



**TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (Each IGBT)**



**TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (Each FWDi)**



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