

# 50MHz to 850MHz, CASCADABLE ACTIVE BIAS InGAP HBT MMIC AMPLIFIER

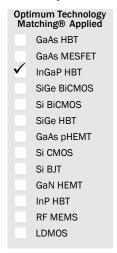
Package: SOT-89

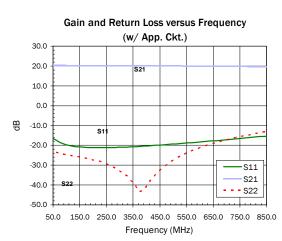




#### **Product Description**

RFMD's SBB2089Z is a high performance InGaP HBT MMIC amplifier utilizing a Darlington configuration with an active bias network. The active bias network provides stable current over temperature and process Beta variations. Designed to run directly from a 5V supply, the SBB2089Z does not require a dropping resistor as compared to typical Darlington amplifiers. The SBB2089Z product is designed for high linearity 5V gain block applications that require small size and minimal external components. It is internally matched to  $50\,\Omega$ .





#### **Features**

- OIP<sub>3</sub>=42.8dBm at 240MHz
- P<sub>1dB</sub>=20.8dBm at 500MHz
- Single Fixed 5V Supply
- Robust 2000V ESD, Class 2
- Patented Thermal Design and Bias Circuit
- Low Thermal Resistance

#### **Applications**

- Receiver IF Amplifier
- Cellular, PCS, GSM, UMTS
- Wireless Data, Satellite Terminals

Baramatar		Specification		Lloit	O and dition	
Parameter	Min.	Тур.	Max.	Unit	Condition	
Small Signal Gain		20.0		dB	70MHz	
	18.5	20.0	21.5	dB	240 MHz	
	18.5	20.0	21.5	dB	400MHz	
Output Power at 1dB Compression		20.0		dBm	70 MHz	
		20.0		dBm	240 MHz	
	18.5	21.0		dBm	400 MHz	
Third Order Intercept Point		41.0		dBm	70 MHz	
		43.0		dBm	240 MHz	
	39.0	41.0		dBm	400MHz	
Return Loss		50 to 850		MHz	Minimum 10dB	
Input Return Loss	15.0	20.0		dB	70 MHz to 5000 MHz	
Output Return Loss	11.0	14.0		dB	70 MHz to 5000 MHz	
Noise Figure		2.7	3.7	dB	500 MHz	
Reverse Isolation		22.0		dB	70 MHz to 5000 MHz	
Thermal Resistance		48.8		°C/W	junction - lead	
Device Operating Voltage		5.0	5.3	V		
Device Operating Current	82.0	90.0	98.0	mA		

 $\label{eq:conditions: VD = 5V, ID = 90 mA Typ., OIP_3 Tone Spacing = 1 MHz, P_{OUT} per tone = 0 dBm, T_L = 25 °C, Z_S = Z_L = 50 \Omega, Tested with Bias Teest Conditions: V_D = 5V, ID = 90 mA Typ., OIP_3 Tone Spacing = 1 MHz, P_{OUT} per tone = 0 dBm, T_L = 25 °C, Z_S = Z_L = 50 \Omega, Tested with Bias Teest Conditions: V_D = 5V, ID = 90 mA Typ., OIP_3 Tone Spacing = 1 MHz, P_{OUT} per tone = 0 dBm, T_L = 25 °C, Z_S = Z_L = 50 \Omega, Tested with Bias Teest Conditions: V_D = 5V, ID = 90 mA Typ., OIP_3 Tone Spacing = 1 MHz, P_{OUT} per tone = 0 dBm, T_L = 25 °C, Z_S = Z_L = 50 \Omega, Tested with Bias Teest Conditions: V_D = 5V, ID = 90 mA Typ., OIP_3 Tone Spacing = 1 MHz, P_{OUT} per tone = 0 dBm, T_L = 25 °C, Z_S = Z_L = 50 \Omega, Tested with Bias Teest Conditions = 1 MHz, P_{OUT} per tone = 0 dBm, T_L = 25 °C, Z_S = Z_L = 50 \Omega, Tested with Bias Teest Conditions = 1 MHz, P_{OUT} per tone = 0 dBm, T_L = 25 °C, Z_S = 2 MHz, P_{OUT} per tone = 0 dBm, T_L = 25$ 

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#### **Absolute Maximum Ratings**

•		
Parameter	Rating	Unit
Device Current (I <sub>D</sub> )	110	mA
Device Voltage (V <sub>D</sub> )	5.5	V
RF Input Power	24	dBm
Junction Temp (T <sub>J</sub> )	+150	°C
Operating Temp Range (T <sub>L</sub> )	-40 to +85	°C
Storage Temp	+150	°C
Power Dissipation	0.61	W
ESD Rating - Human Body Model (HBM)	Class 2	
Moisture Sensitivity Level	MSL2	

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one. Bias Conditions should also satisfy the following expression:  $I_DV_D < (T_J - T_L) / \, R_{TH}, \, j\text{-}I \text{ and } T_L = T_{LEAD}$ 



#### Caution! ESD sensitive device.

Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

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Typical RF Performance at Key Operating Frequencies (With Application Circuit)

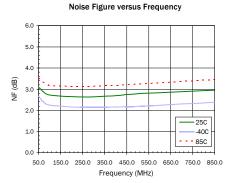
Parameter	Unit	50MHz	70MHz	100	240	400	500	850
				MHz	MHz	MHz	MHz	MHz
Small Signal Gain, S <sub>21</sub>	dB	20.0	20.0	20.0	20.0	20.0	20.0	20.0
Output Third Order Intercept Point, OIP <sub>3</sub>	dBm	40.0	40.0	41.0	42.0	41.0	40.0	35.0
Output Power at 1dB Compression, P <sub>1dB</sub>	dBm	20.0	20.0	20.0	20.0	20.0	20.0	19.0
Input Return Loss, IRL	dB	15.0	18.0	19.0	20.0	20.0	19.0	16.0
Output Return Loss, ORL	dB	21.0	23.0	24.0	27.0	34.0	30.0	14.0
Reverse Isolation, S <sub>12</sub>	dB	22.0	22.0	22.0	22.0	22.0	22.0	22.0
Noise Figure, NF	dB	3.1	2.9	2.7	2.6	2.7	2.8	2.9

Test Conditions:  $V_{CC}=5V = I_D=90 \text{ mA Typ.}$  OIP<sub>3</sub> Tone Spacing=1MHz,  $P_{OUT}$  per tone=0dBm  $T_L=25 ^{\circ}\text{C}$   $Z_S=Z_L=50 \Omega$ 

30.0

25.0

#### Data on charts taken with Application Circuit

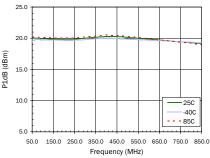


# OIP3 versus Frequency 50.0 45.0 40.0 80 35.0

25C

-400 850

#### P1dB versus Frequency

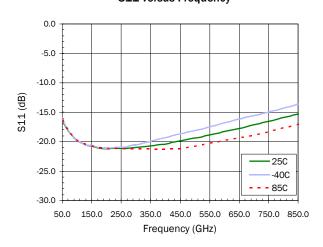


50.0 150.0 250.0 350.0 450.0 550.0 650.0 750.0 850.0 Frequency (MHz)

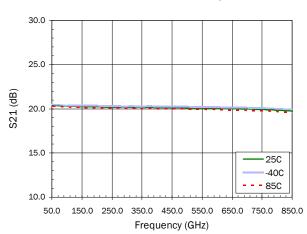


#### **Application Circuit S-Parameters Over Temperature**

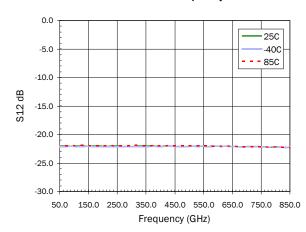
#### S11 versus Frequency



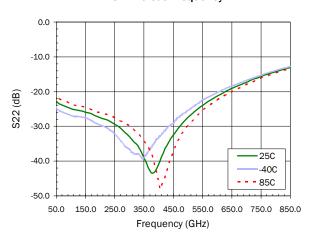
#### **S21** versus Frequency



#### S12 versus Frequency

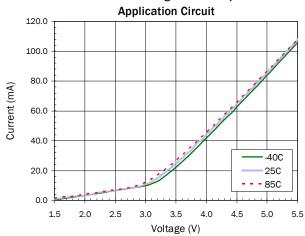


#### S22 versus Frequency



#### **Device Current Over Temperature with Application Circuit**

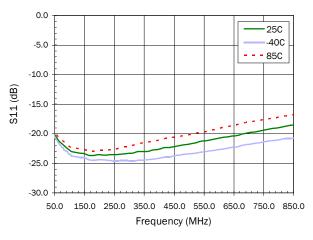
#### **Current versus Voltage Over Temperature**



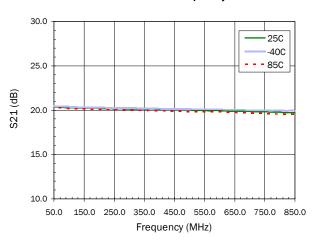
# **SBB2089Z**



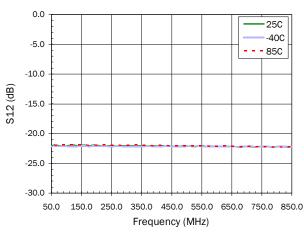
#### S-Parameters Over Temperature (Bias Tee) \$11 versus Frequency



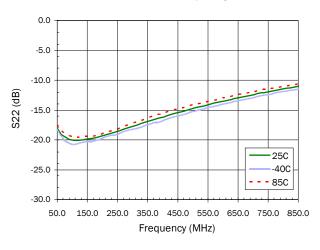
#### S21 versus Frequency



S12 versus Frequency



S22 versus Frequency



#### **Device Current Over Temperature with Application Circuit**

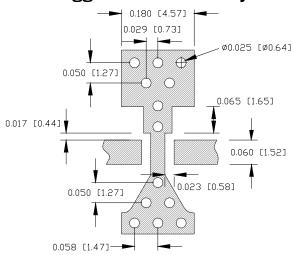
#### **Current versus Voltage Over Temperature Application Circuit** 120.0 100.0 80.0 Current (mA) 60.0 40.0 -40C 20.0 25C 85C 0.0 3.0 3.5 5.0 1.5 2.0 2.5 4.5 5.5 Voltage (V)



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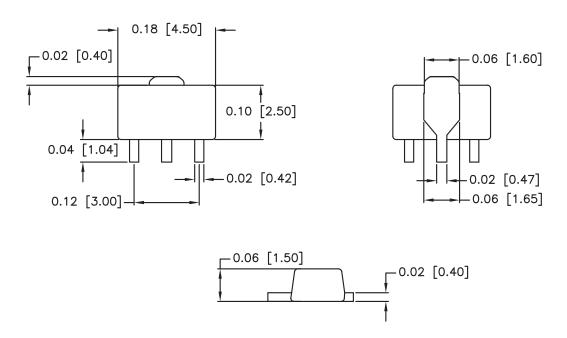
Pin	Function	Description
1	RF IN	RF input pin. This pin requires the use of an external DC blocking capacitor chosen for the frequency of operation.
2, 4	GND	Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.
3	RF OUT/BIAS	RF output and bias pin. DC voltage is present on this pin, therefore a DC blocking capacitor is necessary for proper operation.

### **Suggested PCB Pad Layout**



# **Package Drawing**

Dimensions in inches (millimeters)
Refer to drawing posted at www.rfmd.com for tolerances.



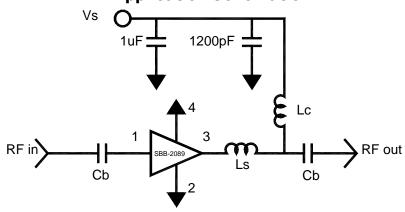
# **SBB2089Z**



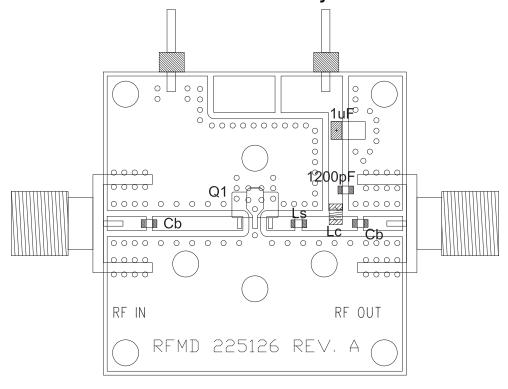
**Application Circuit Element Values)** 

Reference Designator	Frequency (MHz) 50 to 850				
СВ	8200 pF				
LC	1500 nH 0805LS Coilcraft				
LS	2.7 nH Toko				

# **Application Schematic**

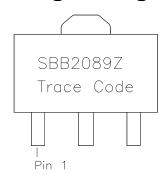


# **Evaluation Board Layout**





# **Package Marking**



# **Ordering Information**

Ordering Code	Description
SBB2089Z	7" Reel with 1000 pieces
SBB2089ZSQ	Sample bag with 25 pieces
SBB2089ZSR	7" Reel with 100 pieces
SBB2089ZPCK1	50MHz to 850MHz PCBA with 5-piece sample bag