

PRODUCT SUMMARY

SKY77346 iPAC™ PA Module for Quad-Band GSM / GPRS

Applications

- Quad-Band cellular handsets encompassing
 - Class 4 GSM850/900
 - Class 1 DCS1800/PCS1900
 - Class 12 GPRS multi-slot operation

Features

- Low input power range
 - 0 dBm to 6 dBm
- High efficiency
 - 52% CELL
 - 52% GSM
 - 52% DCS
 - 52% PCS
- BiCMOS PA controller and interface IC
 - Low power control slope
 - Fast response time
 - Improved control accuracy
 - Overvoltage Shutdown Feature
- Integrated closed loop power amplifier control
- Internal I_{CC} sense resistor for PAC
- Input/Output matching 50 Ω internal (with DC blocking)
- Small, low profile package
 - 5 mm x 6 mm x 0.9 mm
 - 26-pad configuration
- MSL3/260 °C

Description

The SKY77346 Power Amplifier Module (PAM) is designed in a low profile (0.9 mm), compact form factor for quad-band cellular handsets comprising GSM850/900, DCS1800, and PCS1900 operation. The PAM also supports Class 12 General Packet Radio Service (GPRS) multi-slot operation.

The module consists of separate GSM PA and DCS1800/PCS1900 PA blocks, impedance-matching circuitry for 50 Ω input and output impedances and a Power Amplifier Control (PAC) block with an internal current-sense resistor. The custom BiCMOS integrated circuit provides the internal PAC function and interface circuitry. Fabricated onto a single Gallium Arsenide (GaAs) die, one Heterojunction Bipolar Transistor (HBT) PA block supports the GSM bands and the other supports the DCS1800 and PCS1900 bands. Both PA blocks share common power supply pads to distribute current. The GaAs die, the Silicon (Si) die, and the passive components are mounted on a multi-layer laminate substrate. The assembly is encapsulated with plastic overmold.

RF input and output ports of the SKY77346 are internally matched to a 50 Ω load to reduce the number of external components for a quad-band design. Extremely low leakage current (2.5 μA, typical) of the dual PA module maximizes handset standby time. The SKY77346 also contains band-select switching circuitry to select GSM (logic 0) or DCS/PCS (logic 1) as determined from the Band Select (BS) signal. As shown in Figure 1 below, the BS pad selects between the GSM RF input and output pads versus the DCS/PCS RF input and output pads while the Analog Power Control (VRAMP) controls the output power level.

The VCC pad connects to an internal current-sense resistor and interfaces to an integrated power amplifier control (iPAC™) function, which is insensitive to variations in temperature, power supply, process, and input power. The BiCMOS controller will shutdown the PA when VCC is raised above the specified operating voltage.

The SKY77346 is compatible with logic levels from 1.2 V to VCC for BS and ENABLE pads, depending on the level applied to the VLOGIC pad. This feature provides additional flexibility for the designer in the selection of FEM interface control logic.

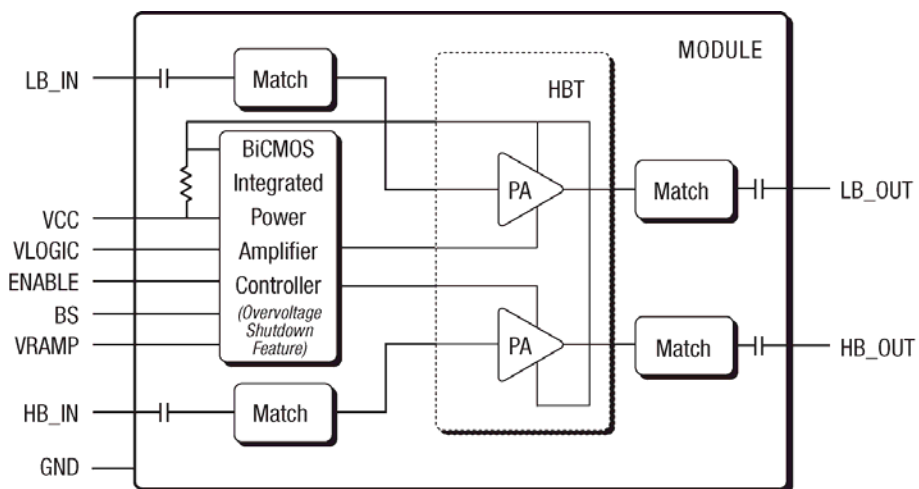


Figure 1. Functional Block Diagram

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