
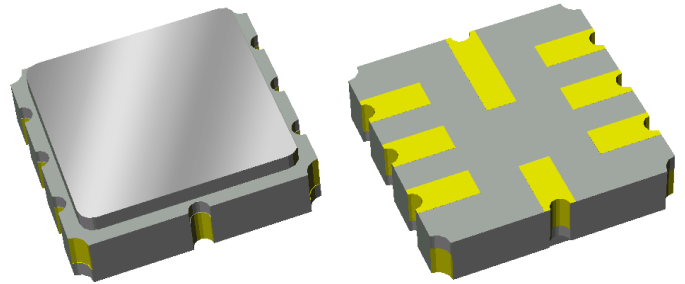


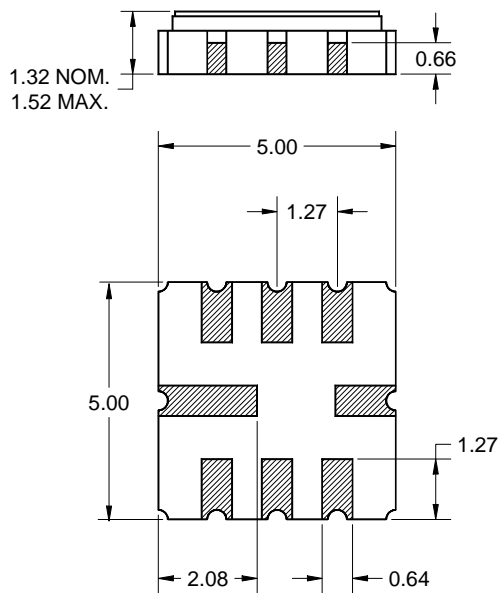
Features

- For WCDMA applications
- Usable 1 dB bandwidth of 20 MHz
- Low loss
- Single-ended operation at 50 Ω
- Single-ended input \ balanced output at 200 Ω
- Ceramic Surface Mount Package (SMP)
- Hermetic
- **RoHS** compliant (2002/95/EC), **Pb-free** 



Package

Surface Mount 5.00 x 5.00 x 1.32 mm
SMP-20A

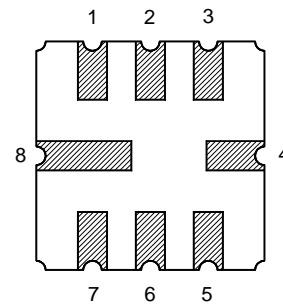


Dimensions shown are nominal in millimeters
All tolerances are ± 0.15 mm except overall
length and width $+0.15/-0.10$ mm

Body: Al_2O_3 ceramic
Lid: Kovar, Ni plated
Terminations: Au plating 0.5 - 1.0 μ m,
over a 2 - 6 μ m Ni plating

Pin Configuration

Bottom View



SE - Balanced Configuration

Pin No.	Description
7	Input +
3	Output +
2	Output -
1,6,5	Ground
4,8	Case Ground

Single-ended Configuration

Pin No.	Description
3	Input
7	Output
1,2,5,6	Ground
4,8	Case Ground

Electrical Specifications ⁽¹⁾

Operating Temperature Range: ⁽²⁾ -20 to +80 °C

Parameter ⁽³⁾	Minimum	Typical ⁽⁴⁾	Maximum	Unit
Center Frequency	-	168.5	-	MHz
Minimum Insertion Loss 158.5 – 178.5 MHz	-	8.0	9.0	dB
1.0 dB Bandwidth	22	26.4	-	MHz
Amplitude Variation 158.5 – 178.5 MHz	-	0.6	1.0	MHz
Group Delay Ripple 158.5 – 178.5 MHz 160.5 – 176.5 MHz	- -	20 20	40 40	μsec μsec
Relative Attenuation ⁽⁵⁾ 10 – 100 MHz 100 – 149 MHz 149 – 150.5 MHz 150.5 – 153.5 MHz 183.5 – 186.5 MHz 186.5 – 190 MHz 190 – 250 MHz 250 – 310 MHz 310 – 410 MHz 410 – 1000 MHz	50 33 20 3 2 19 33 40 50 55	61 41 32 6.5 4.5 32 42 56 59 59	- - - - - - - - - -	dB dB dB dB dB dB dB dB dB dB
Input/Output Return Loss 158.5 – 178.5 MHz	10	14	-	dB
EVM 158.5 – 163.5 MHz 163.5 – 168.5 MHz 168.5 – 173.5 MHz 173.5 – 178.5 MHz	- - - -	1.2 1.2 1.2 1.3	2.5 2.6 2.5 2.5	% % % %
Source Impedance (single-ended) ⁽⁶⁾	-	50	-	Ω
Load Impedance (single-ended) ⁽⁶⁾	-	50	-	Ω
Load Impedance (balanced) ⁽⁶⁾	-	200	-	Ω

Notes:

1. All specifications are based on the TriQuint test circuit matching schematics shown on page 5
2. In production, devices will be tested at room temperature to a guardbanded specification to ensure electrical compliance over temperature
3. Electrical margin has been built into the design to account for the variations due to temperature drift and manufacturing tolerances
4. Typical values are based on average measurements at room temperature
5. Relative to minimum insertion loss
6. This is the optimum impedance in order to achieve the performance shown

Electrical Specifications ⁽¹⁾

Operating Temperature Range: ⁽²⁾ -40 to +85 °C

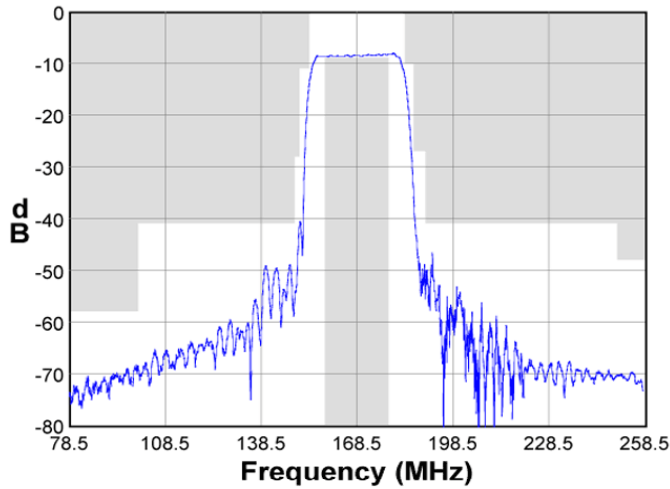
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Source Impedance (single-ended) ⁽⁶⁾	-	50	-	Ω
Load Impedance (single-ended) ⁽⁶⁾	-	50	-	Ω
Load Impedance (balanced) ⁽⁶⁾	-	200	-	Ω

Notes:

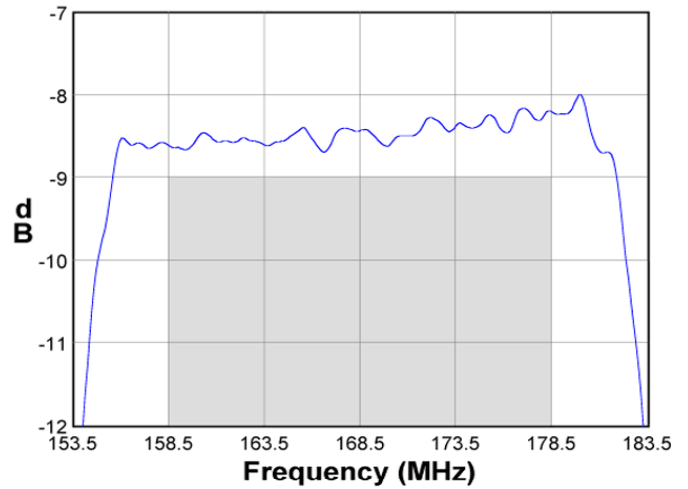
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Typical Performance (at room temperature)

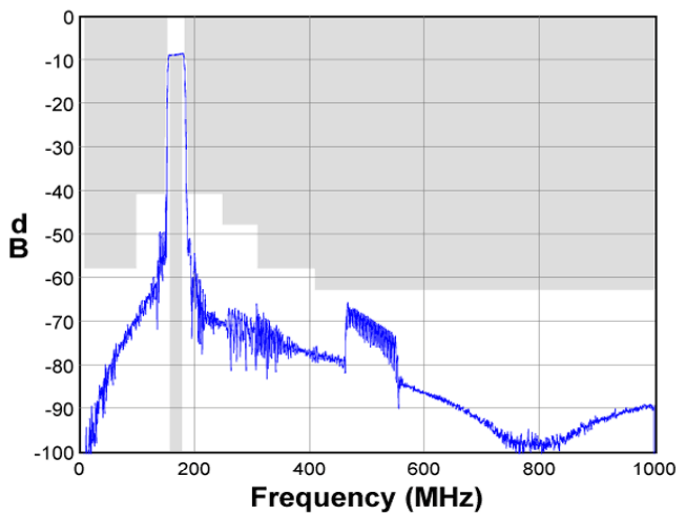
Frequency Response



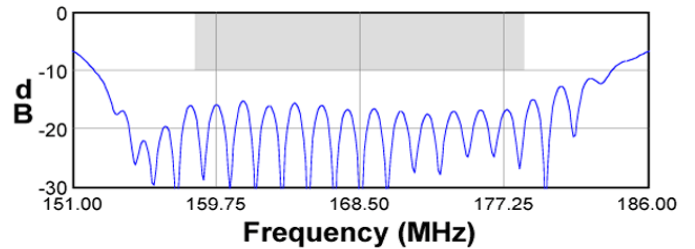
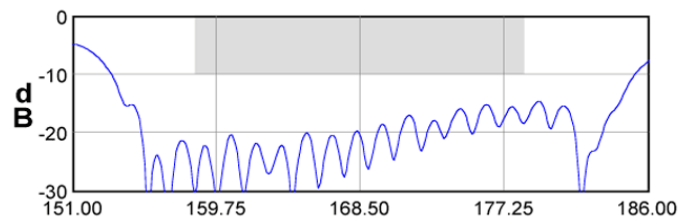
Passband Response



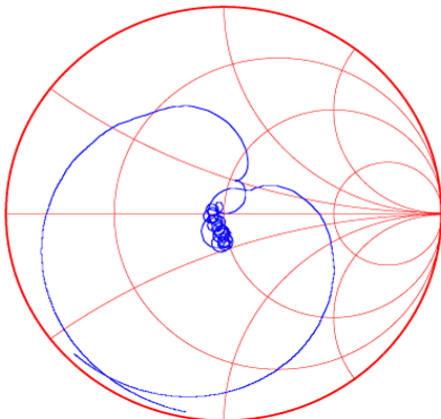
Wideband Response



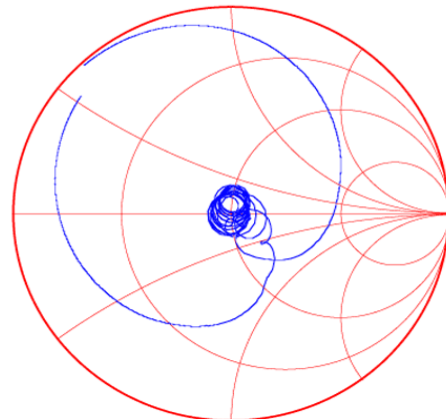
Input/Output Return Loss



Input Smith Chart



Output Smith Chart




Maximum Ratings


Parameter	Symbol	Minimum	Maximum	Unit
Operating Temperature Range	T	-40	+85	°C
Storage Temperature Range	T _{stg}	-40	+85	°C

Important Notes

Warnings

- Electrostatic Sensitive Device (ESD) 
- Avoid ultrasonic exposure

RoHS Compliance

- This product complies with EU directive 2002/95/EC (RoHS) 

Solderability

- Compatible with JESD22-B102, Pb-free process, 260C peak reflow temperature ([see soldering profile](#))

Links to Additional Technical Information

[PCB Layout Tips](#)

[Qualification Flowchart](#)

[Soldering Profile](#)

[S-Parameters](#)

[RoHS Information](#)

[Other Technical Information](#)

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