




Product Anomaly Notification (PAN)

Device affected (product name): nRF8002	Active device version(s): BX
Date (YYYY-MM-DD): 2012-06-04	PAN no.: PAN-026
Nordic Semiconductor reference: Thomas Bonnerud	Document version: 1.0

Anomalies in active device version(s)

Summary: 1. Sleep on disconnect 2. UART pins 3. GPIO pins 4. Battery service						
Marking / tracing: <div style="border: 1px solid black; width: 80px; height: 60px; display: flex; align-items: center; justify-content: center; margin: 5px 0;"> <div style="text-align: center; font-size: 8px; line-height: 1;"> <small>o</small> NRF BX 8002 YYWLL </div> </div> <p>Build: BX</p> <p>Where the letters on the last line of the chip marking means:</p> <ul style="list-style-type: none"> • Y = Year assembly marking, e.g. YY=12 • W = Week assembly marking, e.g. WW=35 • L = Wafer lot, step letters for each lot, e.g. LL={AA, AB,...,AZ, BA,...,ZZ, AA, AB,...} 						
Authorization for Nordic Semiconductor <table style="width: 100%; border: none;"> <tr> <td style="width: 30%;">Product Manager</td> <td style="width: 30%;">Date:</td> <td style="width: 40%;">Sign:</td> </tr> <tr> <td>Thomas Embla Bonnerud</td> <td>04.06.2012</td> <td style="text-align: center;"></td> </tr> </table>	Product Manager	Date:	Sign:	Thomas Embla Bonnerud	04.06.2012	
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1. Sleep on disconnect	Device version(s) affected: BX
Symptoms: If a peer device does a controlled terminate, then nRF8002 enters sleep mode.	
Conditions: If any device connects and then does a controlled terminate the nRF8002 will enter sleep mode.	
Consequences: Initial feedback from the market indicates this is poor behavior.	
Workaround: No workaround. This designed behavior will be changed in build code B by nRF8002 starting to advertise instead of entering sleep mode.	

2. UART pins	Device version(s) affected: BX
Symptoms: UART pins not defined after final OTP programming.	
Conditions: After final OTP configuration	
Consequences: Possible high leakage currents in low power modes due to floating inputs	
Workaround: Use pull down resistors on the UART pins. This anomaly will be fixed in build code B.	

3. GPIO pins	Device version(s) affected: BX
Symptoms: GPIOs can only source/draw 0.5 mA	
Conditions: Standard behaviour	
Consequences: None	
Workaround: No workaround. This design behavior will be configurable in build code B by allowing high drive feature, i.e. 5 mA.	

4. Battery service	Device version(s) affected: BX
Symptoms: Battery service always reports 100%.	
Conditions:	
Consequences:	
Workaround: No workaround. This anomaly will be fixed in build code B.	