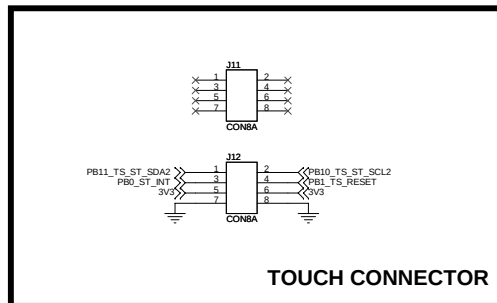
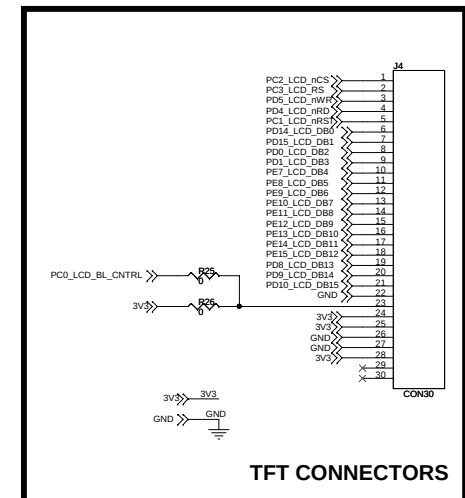
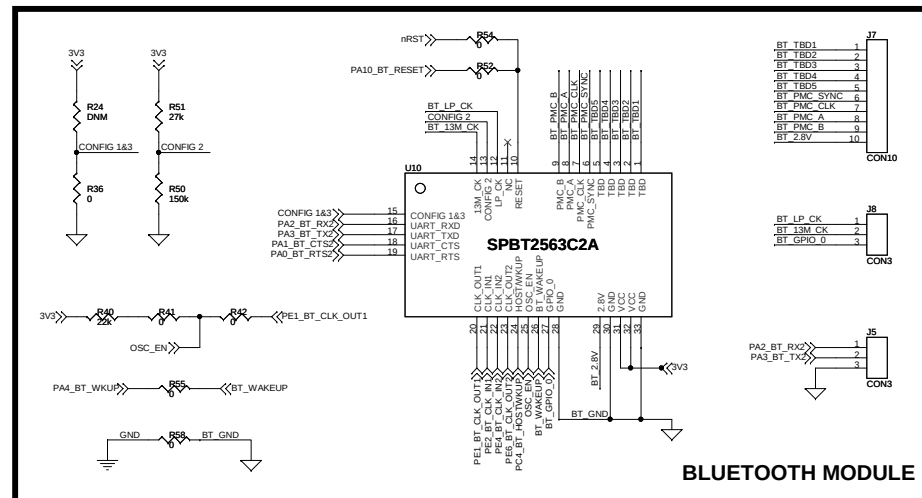
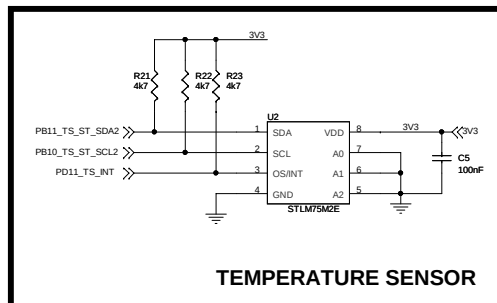
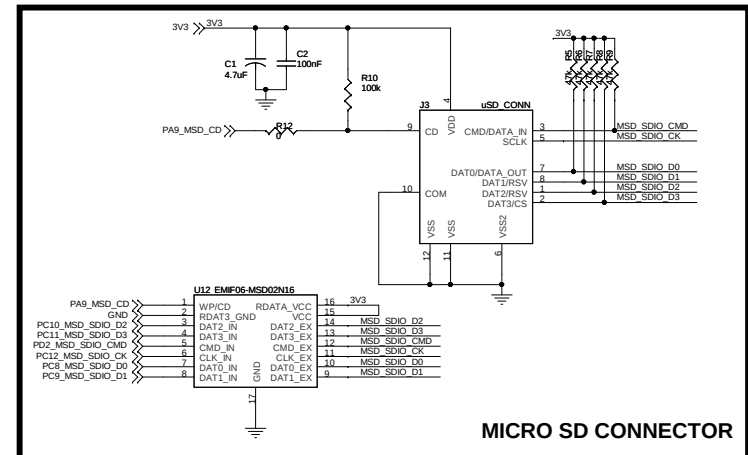
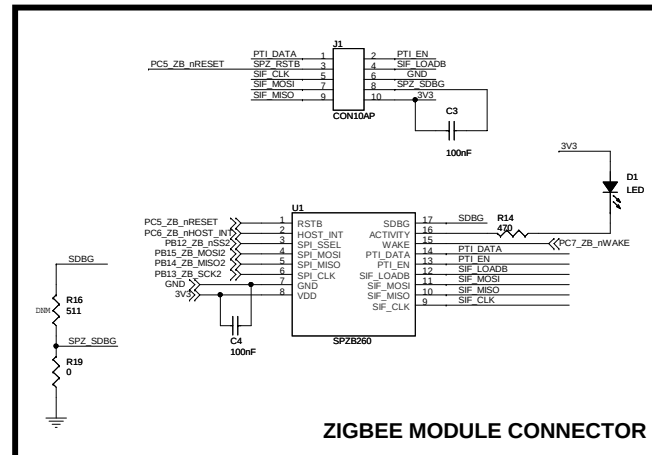
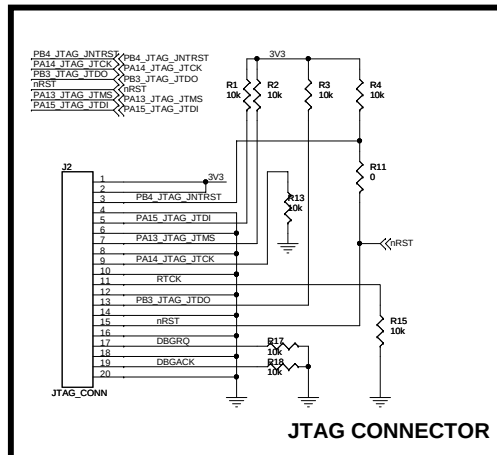
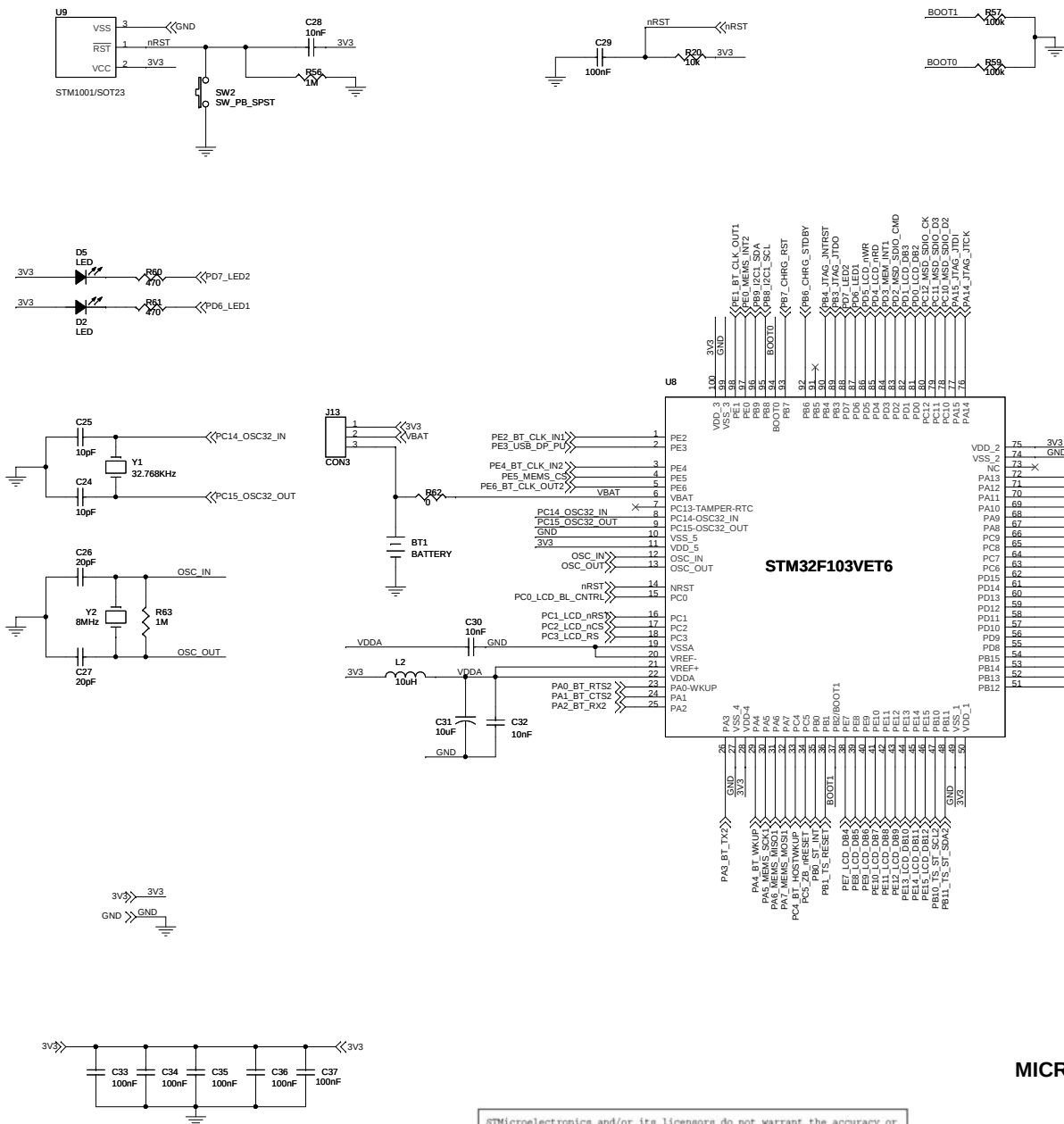




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The schematic diagram illustrates the STW4102 Battery Charger circuit, divided into three main sections:

- Main Charging Section:** The STW4102 IC (U3) is the central component. It is configured with various pins connected to a USB input (USBSV) and a battery (BATTERY). Key components include:
 - Input:** USB input (USBSV) connected to pins 1 (MAIN), 3 (USBPWR), and 24 (PWRDETECTN). A 3V3 supply is connected to pins 28 (PD12_CHRG_PWDT) and 34 (PA8_EOCHG).
 - Output:** The battery (BATTERY) is connected to pins 11 (BATT-), 12 (BATT+), and 20 (BATTSENSE).
 - Control:** Pins 8 (CVREF), 10 (ISET), 13 (C32KHZ), 14 (STDBY), 17 (VIO), 18 (SCL), 19 (SDA), and 22 (RESETN) are connected to various control signals and ground.
 - Other:** Pins 2 (VOUT), 16 (SENSE), and 23 (TEST) are also connected.
- Power Switch Section:** A power switch (SW1) is connected to the battery (BATT+ and BATT-). The switch is controlled by a 3V3 supply. The switch output is connected to the USB input (USBSV) through a 1k resistor (R44) and a 1M resistor (R45) to ground.
- Power Section:** The power section includes a 5V input (L6920_IN) connected to a 1k resistor (R37) and a 100nF capacitor (C12). The output is connected to a 3V3 supply through a 100nF capacitor (C13) and a 100nF capacitor (C14).

[illegible]

The schematic diagram illustrates the USB section of a circuit. It features a USB MiniB Type connector (J6) with pins 6 (SHELL, USB_VCC), 7 (SHELL, USBDM), 8 (SHELL, USBDM), 9 (SHELL, USB DP), ID (SHELL), and USB_GND (5). The connector is connected to a USB_LC2P2E chip (U6). The chip's pins are connected as follows: I/O1 (1) to USB_VCC, I/O2 (2) to USBDM, I/O3 (3) to USBDM, I/O4 (4) to USB DP, and I/O5 (5) to USB_GND. The chip is also connected to a USB_BSV source (U5) via USB DM and USB DP lines. The USB_BSV source is connected to a USB_VCC source (U4) via a 100nF capacitor (C17) and a 10uF capacitor (C18). The USB_VCC source is connected to a 1M resistor (R48) and a 4.7nF capacitor (C21). The USB_LC2P2E chip is connected to a USB_DP_PU source (U3) via a 1.5k resistor (R49) and a 15pF capacitor (C20). The USB_DP_PU source is connected to a USB_DM source (U2) via a 33 ohm resistor (R46) and a 15pF capacitor (C19). The USB_DM source is connected to a USB_DP source (U1) via a 33 ohm resistor (R47) and a 15pF capacitor (C20). The USB_DP source is connected to a USB_DM source (U0) via a 33 ohm resistor (R46) and a 15pF capacitor (C19). The USB_DM source is connected to a USB_DP source (U0) via a 33 ohm resistor (R46) and a 15pF capacitor (C19).

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