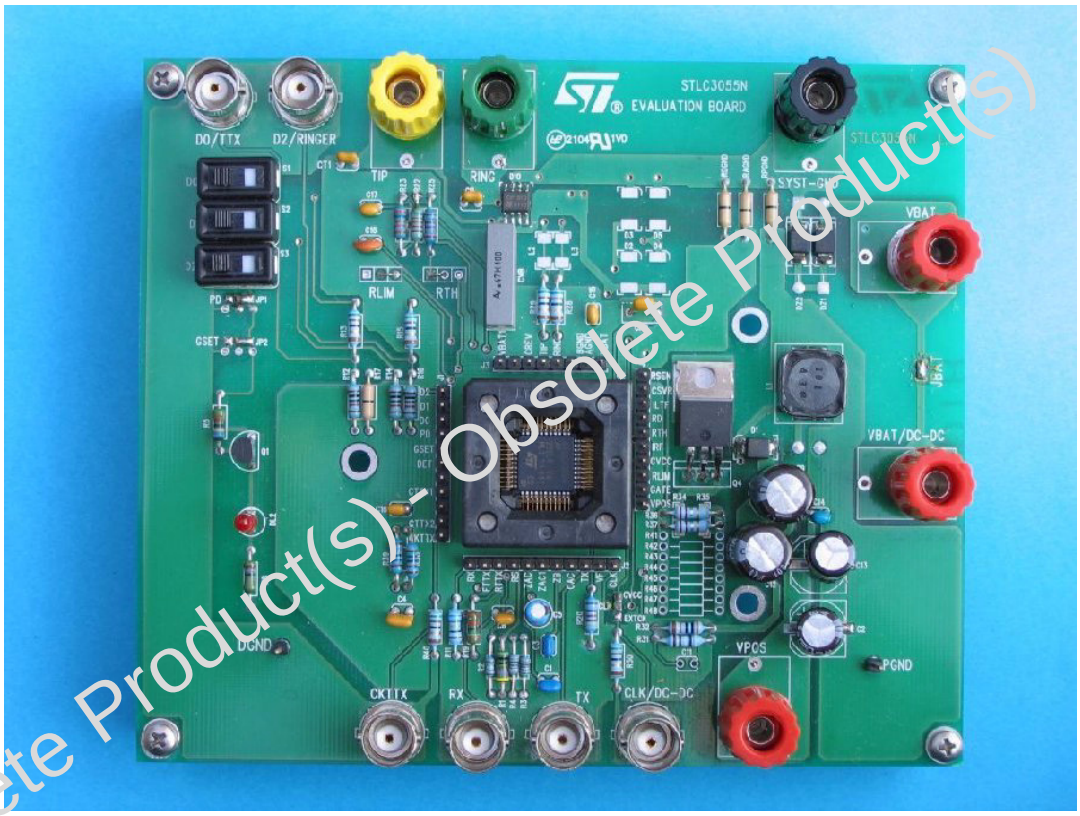
# UM0270
# User manual

## Evaluation board with STLC3055N SLIC for WLL


## Abstract

This document is a simple user manual of the STLC3055N evaluation board, it is not a description of the device functionalities.

This description is available in the STLC3055N datasheet and Application note (AN2117) documents.

# Contents

# 1 STLC3055N Evaluation board description

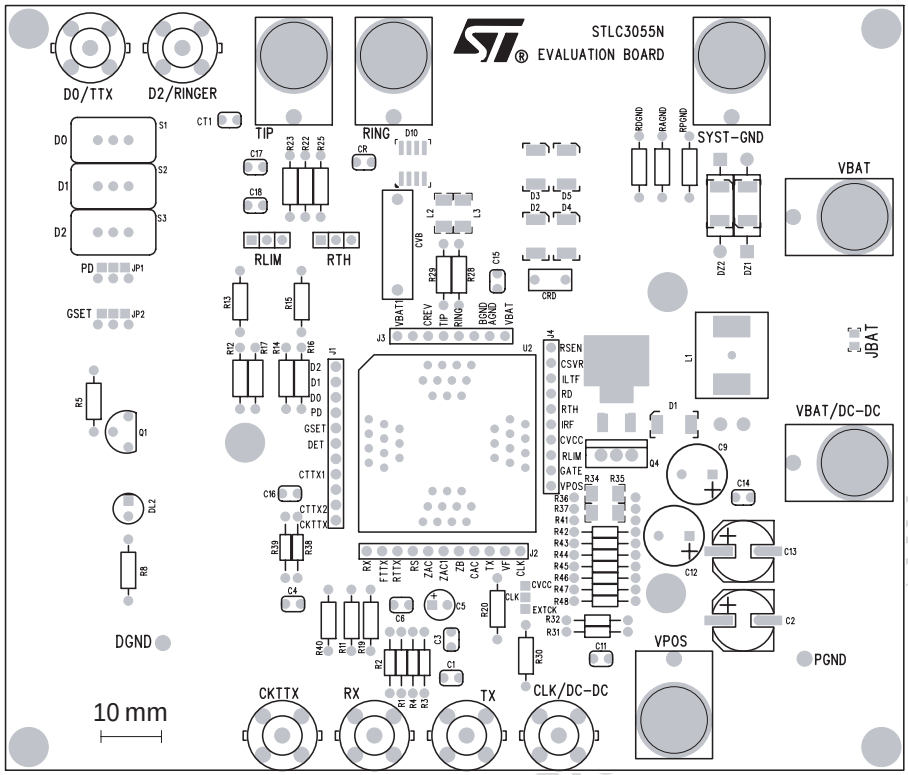
The board is provided of a set of external connectors that permits to test all the functionalities of the STLC3055N chip. In *Figure 1* is shown the placement of the external connectors and components inside the board.

Following the description of these connectors.

- VPOS is the positive supplier voltage provided to the SLIC.
- SYST-GND is the ground of the board.
- VBAT and VBAT/DC-DC are two outputs and they must be connected together by means a short circuit. They allow to monitor the VBAT behavior.
- TIP and RING are the 2W local loop port.
- RX and TX are the 4W port (TX output, RX input)
- D0/TTX is a control bit (used for ringing and TTX injection)
- D2/RINGER is used to generate the ringing signal: it is a square wave input (from 20Hz to 50Hz)
- CKTTX is the metering pulse clock input (12 or 16 KHz square wave).
- CLK/DC-DC is the 125KHz clock input for the DC-DC converter.
  External CLK will arrive on the CLK pin connecting, by a drop of tin, the small area named EXTCK with CLK. Using this type of connection (EXTCK with CLK) and no external CLK signal (125KHz) is present, the DC-DC converter goes in turn-off condition. Instead connecting the CLK with CVCC, it will be possible to operate in autoscillation mode (the board is usually set in this configuration).
- Switches S1, S2, S3 (D0, D1, D2) are used to select the operating mode of the device.

PD, Power Down control pin, or GAIN SET input pin can be connected to the low or high digital level by a drop of tin on the small area. When GAIN SET = 1, some external components have to be modified (see Table 12 on page 12 of the datasheet).

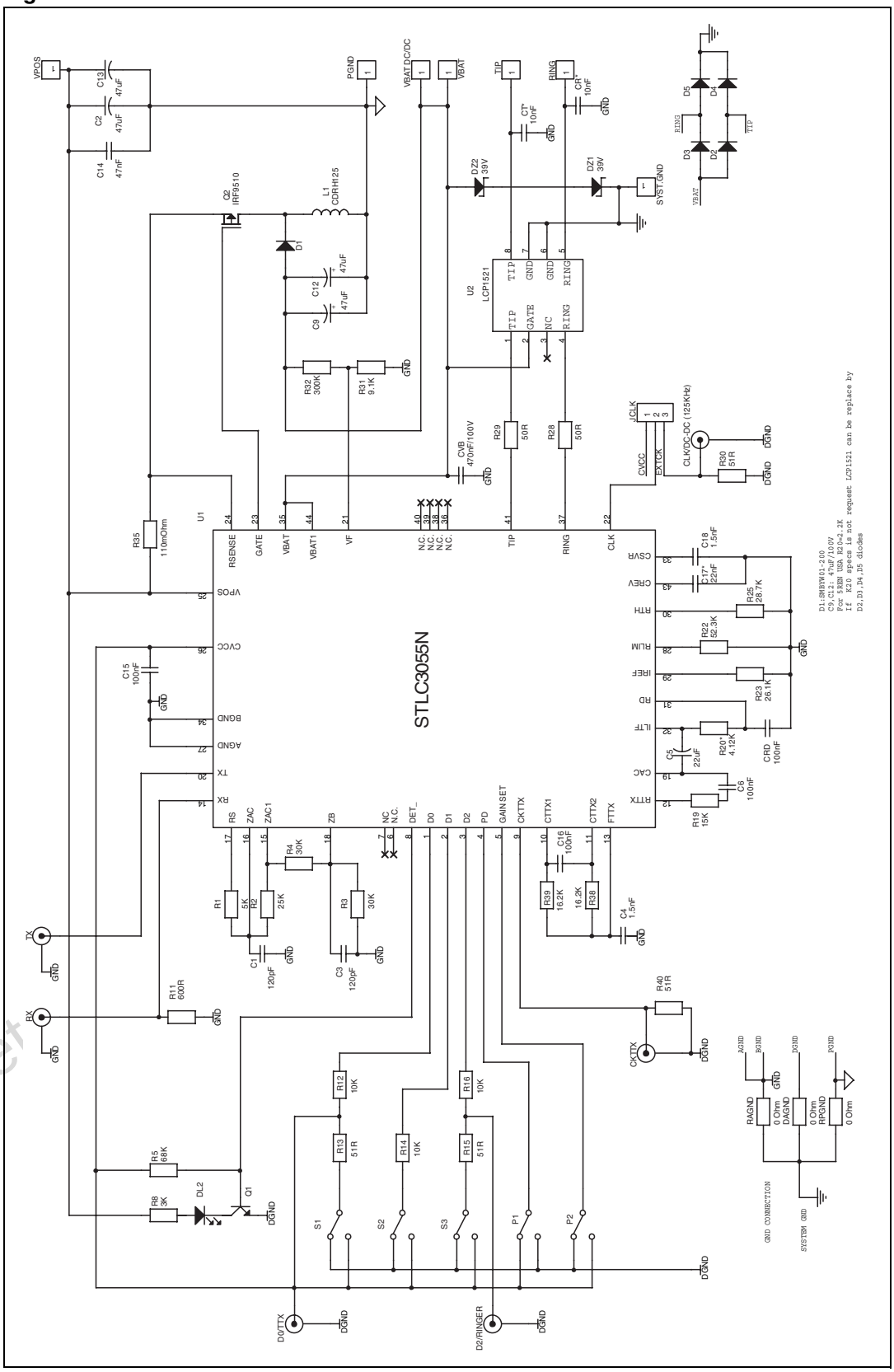
**Figure 1. Component and connectors placement of STLC3055N evaluation board.**




## 1.1 Transition between Ringing and Active mode

The board haven't got the possibility to switch automatically from Ringing to Active mode (automatic Ring Trip detection) but it is necessary to move manually in the correct position the D0 and D1 switches and remove the input square wave applied to D2/RINGER connector.

**Figure 2. Schematic of STLC3055N evaluation board**



# 2 Revision history

Table 1. Document revision history

| Date | Revision | Changes |
|---|---|---|
| 18-Jul-2006 | 1 | Initial release. |

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