



# EasyPath-6 FPGA FAQs

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## **What are EasyPath-6™ FPGAs?**

Xilinx EasyPath-6 FPGAs are the industry's only design-specific FPGA solution to offer seamless cost reduction for complex platform FPGA designs. Xilinx EasyPath-6 FPGAs offer a fast, simple, and risk-free way to migrate your designs to cost-reduced parts.

## **What is the turn-around time for getting cost savings with EasyPath-6™ FPGAs?**

EasyPath-6 FPGAs offer dramatic improvements in turn-around-time over previous generation of EasyPath devices. With EasyPath-6 FPGAs, cost savings are realized in mere six weeks from design freeze. The time-to-cost-reduction offered by EasyPath-6 FPGAs is substantially better than Structured ASIC offerings.

## **Is there an equivalent EasyPath-6 FPGA for each Virtex-6 FPGA? Or is there only a small set of EasyPath-6 FPGAs to choose from?**

There is an equivalent EasyPath-6 FPGA for all Virtex-6 LXT, SXT, and HXT devices (all packages, all speed grades, and all temperature grades). EasyPath-6 FPGAs offer a more complete cost-reduction solution compared to any Structured ASIC.

## **Do I have to limit my design with additional constraints to qualify for EasyPath-6™ FPGA cost reduction?**

Unlike FPGA –to-Structured-ASIC design flow, EasyPath-6 solution requires absolutely no additional constraints on your FPGA design. You can utilize all resources in the Virtex-6 FPGA for your application, and be able to migrate to EasyPath-6 FPGA seamlessly.

## **Will the Soft and Hard IP blocks work exactly the same way in EasyPath-6 FPGAs?**

EasyPath-6 FPGAs leverage the same portfolio of industry-leading soft and hard IP



as Virtex-6 FPGAs. All Virtex-6 FPGA IPs are guaranteed to work in EasyPath-6 FPGAs without any modifications.

**Is this a configurable device?**

Yes, it is. EasyPath FPGAs are architecturally identical to Virtex FPGAs, and will configure at power-up with the design-specific bitstream.

**Do I have to sign up for Minimum Order Quantities with EasyPath-6 FPGAs?**

EasyPath-6 FPGAs can be ordered without the constraints of ASIC-like Minimum Order Quantities (MOQ). EasyPath-6 devices can now be ordered in the same minimum order quantities as standard FPGAs. Freedom from MOQs and inventories enables quick response to changing market forecasts and better cost savings.

**What level of cost reduction can I expect from EasyPath-6 FPGAs?**

EasyPath-6 cost reduction solution delivers the lowest total product cost by reducing or eliminating costs in the varied phases of a product life cycle. EasyPath-6 FPGA lowers device cost by 35%, and reduces silicon re-design cost to zero. Additionally, EasyPath-6 FPGAs drive costs for re-qualification, re-characterization, and board re-design to zero. The fastest time-to-market delivers immediate cost savings and more value compared to any competing solution. With the No-MOQ offering, EasyPath-6 FPGA also minimizes up-front inventory cost and the cost of writing down unwanted inventory. Also, the most valuable cost saving often accrues from the ability to deploy valuable resources on next generation development as opposed to cost reducing existing products.

**What are the NRE charges for EasyPath-6 FPGA?**

There is a \$300k custom charge for cost reducing to EasyPath-6 FPGAs. The custom charge is applied towards custom testing, processing, and inventory management for the industry leading six week turnaround.

**Where can I get more information about ordering EasyPath-6 FPGAs?**

Please contact your local Xilinx Sales Representative for ordering information.